Dr. ATUL KUMAR DWIVEDI

⌘Member IEEE (SPS), IEANG, ISTE, ISCA

⌘Assistant Professor, BIET Jhansi

Educational qualifications

- 2017: **Ph. D**., NIT Raipur, India
- 2012: Qualified UGC **NET** in Electronics
- 2011: **M.Tech**., VLSI Design, VNIT, Nagpur, India
- 2009: Qualified all India **GATE** exam

Work Experience

- Teaching :(1) Bundelkhand Institute of Engineering and Technology, Jhansi (India) from December 2018 to present (2) Bhilai Institute of Technology, Durg (India) from July 2012 to December 2017, taught-basic electronics, Analog Electronics, digital electronics, Signals and Systems, Optical Communication. (3) Rungta College of engineering and technology, Bhilai (India) from July 2011 to July 2012, (4) Selected as post doctoral fellow in Indian Institute of Technology, New Delhi on 15 Jan 2018 (5) Selected in Tata Consultancy Services on 29 November 2010
- Research: (1) VNIT Nagpur, National MEMS design centre, research scholar and teaching assistant, July 2009 to June 2011, (2) NIT Raipur, Junior research fellow: August 2013 to September 2015; Senior research fellow till October 2016.

Brief Overview

Dr. Dwivedi received Ph. D. in specialization of Signal processing and M. Tech. in VLSI Design from NIT Raipur and VNIT Nagpur, India respectively. He always has shown meritorious performance in his academics. He is an active researcher in the area of VLSI and digital signal processing. He is working on implementation of evolutionary optimization based multidimensional digital filter design using FPGA. He has been member keynote speaker, member organizing committee and organizing secretary in many national and international conferences. He has published articles in various refereed international journals and

conferences. He has delivered many tutorials, workshops and training programs in the area of evolutionary optimization and their application in various domains of signal and image processing. His main research interests are VLSI, biomedical signal processing, image processing and evolutionary optimization.

Skill Set

- Teaching interest : Electronic Devices and Circuits, Signals and Systems, Integrated Circuits, CMOS Design, MEMS, VLSI Signal and image processing
- Software Language : C, C++, Data structures, MATLAB, Assembly, VHDL, Verilog
- Simulation Tool : Simulink, Comsol multiphysics, Coventorware, Cadence, ModelSim XE III 6.2g, Xilinx ISE
- Research Interests: Evolutionary optimization, Digital Filter design, FPGA, VLSI, Bio-medical signal and Image Processing; MEMS design

Future Research Plan

- **Project 1**.
- Title of the work: Low power Correlation Filter Design for Brain Tumor Detection
- Summary : The detection of brain tumor is one of the challenging task in medical image processing. Based on statistics, tumors are the second cause of cancer related deaths in children who are under the age of 20. The main objective of this research is to provide a computer added software that will help the doctors in saving lives by detecting the tumors earlier and perform necessary actions.

- **Project 2**.
- Title of the work: Low power FIR filter design for early detection and diagnosis of cardiac diseases using Evolutionary optimization based
- Summary : A large percentage of cardiologists' decision in disease assessment is based on observational behaviour. The current computer-aided diagnosis systems have been found to be very useful for assisting cardiologists to understand the severity of diseases qualitatively and quantitatively. The work proposed in this project aims at designing FIR filters for generating biomedical signals which for diseased and non-diseased persons. The generated FIR filters coefficients will be considered as features for classification of diseased and non-diseased signals. In the recently reported works, FIR filters coefficients have been reported for modelling non-linear signals. In this project the work will be extended for early identification of diseased and non-diseased person. The classification will be performed

using various artificial intelligence based classification techniques like neural networks, fuzzy logic, ANFIS, hybrid genetic algorithms and fuzzy logic etc. To validate the applicability of the proposed work for real time applications, the proposed approach will be implemented on FPGA (virtex-7) and obtained signals after implementation will be compared with its theoretical and numerical proofs. The proposed project will tested on standard databases of cardiac signals available like ECG, PCG and PCG etc.

Research Works Completed

- **Ph.D.**
- Title : Optimization Based Design and Implementation of Digital FIR Filters.
- Duration : 3 years
- Advisor : Dr. Subhojit Ghosh
- Co-Advisor : Dr. Narendra D. Londhe
- Research centre: NIT Raipur
- Summary: The aim of the project was to design and implement filters which not only minimize the ripples in frequency response of a filter but also minimizes the power consumption during filter execution. Filters of various specifications and orders, has been designed and it has been found that these two objectives are conflicting in nature. To deal with the conflicting nature of the objective, a multi-objective optimization approach for filter design has been proposed. None of the previously reported works has utilized the multi-objective optimization for this purpose. The filter design work has been extended for designing two dimensional FIR filters. It has been found that proposed approach not only good for one dimensional filter but also it performs better for two dimensional filters.
- **M.Tech.**
- Title 1 : Portable system for Water pathogen Detection using MEMS .
- Duration: 1 year
- Supervisor : Prof.R.B.Deshmukh
- Co-Supervisor : Prof.R.M.Patrikar
- Research centre: National MEMS design centre , VNIT Nagpur (India)
- Tools : Comsolmultiphysics, Coventorware and Tanner L- edit
- Summary : The aim of project is to design a portable system which detects pathogens present in water as well senses water quality like temperature, pH, turbidity, conductivity, viscosity and total dissolved solids present in the system. We are making a system that can be easily interfaced with the existing CMOS technology and can give results quicker than currently available methods.

Books/Book Chapters

- Book Chapter, Digital Filter Design using Quantum Inspired Multi-Objective Cat Swarm Optimization Algorithm, Book Name: Quantum Inspired Computational Intelligence, ISBN: 9780128044094 imprint Morgan Kaufmann, Elsevier publication, September 2016, pages 327-359.
- Book entitled ,Microfluidic Sensors design for E.Coli Bacteria in Water", Published by Lap lambert academic publishing, Jermany, October, 19, 2011, ISBN: 3845434686

International Journals

- Krishanu Kundu*, Narendra Nath Pathak and Atul Kumar Dwivedi, "Optimization of Linear antenna array using Binary Cat Swarm Optimization (BCSO)", International Journal of Sensors, Wireless Communications and Control (2019) 9: 1. https://doi.org/10.2174/2210327909666190710121921
- Atul Kumar Dwivedi, Subhojit Ghosh, Narendra D. Londhe, Review and Analysis of Evolutionary optimization based techniques for FIR filter design, Circuits, Systems and Signal Processing (Springer Publication), ISSN: 1531-5878 (February 2018)
- Atul Kumar Dwivedi, Babita Sahu, Enhanced Image Object Recognition system using Correlation Filter based on Optimization, International Journal of Scientific Research in Computer Science, Engineering and Information Technology (IJSRCSEIT),UGC approved, ISSN : 2456-3307, Volume 2, Issue 2, pp.895-900, March-April.2017
- Atul Kumar Dwivedi, Subhojit Ghosh, Narendra D. Londhe, Low power 2D FIR Filter Design Using Modified Artificial Bee Colony Algorithm with Experimental Validation Using FPGA, IET Science Measurement and Technology, ISSN:17518830, 17518822, Volume 36 Issue 1, January 2017, Pages 156-180. doi:10.1049/iet-smt.2016.0069. (SCI/Scopus indexed, Impact factor 1.285)
- Atul Kumar Dwivedi, Subhojit. Ghosh, Narendra D. Londhe, A Modified Artificial Bee Colony Optimization Based FIR Filter Design with Experimental Validation Using FPGA, IET Signal Processing (October 2016), Volume 10, issue 8, ISSN: 1751-9675, PP 955-964 doi:10.1049/iet-spr.2015.0214. (SCI/Scopus indexed, Impact factor 1.325)
- Atul Kumar Dwivedi, Subhojit Ghosh, Narendra D. Londhe, Low Power FIR Filter Design Using Hybrid Artificial Bee Colony Algorithm with Experimental Validation Over FPGA, Circuits, Systems and Signal Processing (Springer Publication),Vol 36, Issue 1,ISSN: 1531-5878 (March 2016), pages 1–31. doi:10.1007/s00034-016-0297-4. (SCI/Scopus indexed, Impact factor1.694)

- Atul Kumar Dwivedi, Subhojit Ghosh, Narendra D. Londhe, Low Power FIR Filter Design Using Modified Multi-objective Artificial Bee Colony Algorithm, Engineering Applications of Artificial Intelligence (Elsevier Publication), vol 55, ISSN :0952-1976 (October2016),pages: 58–69. doi:10.1016/j.engappai.2016.06.006. (SCI/Scopus indexed, Impact factor: 2.368)
- Atul Kumar Dwivedi, RaghvendraBhushan, "A Novel Algorithm For Speech Based Authentication Of Digital Images Using DWT", vol 1, issue 7, ISSN: 2348- 4853, international journal of advanced foundation and research in computer, July 2014, page no 54-64.

International Conferences

- Atul Kumar Dwivedi, Shashank Kumar, K Vamsi Krishna, Design of Milk Fat Assessment System for Dairy Farmers, In proceedings of International Conference on Electrical, Electronics, Computers, Communication, Mechanical and Computing (EECCMC), digital library and Priyadarshini Engineering college, Vellore, India, 28th to 29thJanuary 2018, pages: 1-5
- Atul Kumar Dwivedi, Sandeep Kumar Bhatt, Subhojit Ghosh, Fractional order butterworthfilter design using Artificial Bee colony algorithm, in proceedings of IEEE 7th International Symposium on Embedded Computing and System Design (ISED), Organized by IEEE and NIT, Durgapur, India, 18-20 Dec. 2017, page 17 (5), doi :10.1109/ISED.2017.8303938, SCI/ Scopus, ISBN: 247-3-9413
- Atul Kumar Dwivedi, R. Aparna, Sonika Arora, Fractional Order Digital FIR Filter Design using Binary Cat Swarm Optimization, in proceedings of International Conference on Recent Trends in Engineering, Science & Technology - (ICRTEST 2016),2016 , Organized by IET digital library and St. Peter Engineering college, Hydrabad, India, 25th to 27th October 2016, page 17 (5), doi :10.1049/cp.2016.1485, SCI/ Scopus, ISBN: 978-1-78561-785
- SwagotaBera, Monisha Sharma, S. SubramanyaSikhar, Atulkumar Dwivedi, An efficient blind steganalysis using higher order statistics for the neighborhood difference matrix, in proceedings of IEEE international conference on Wireless Communications, Signal Processing and Networking (WiSPNET)March 2016, pp 211-215, Chennai, Indiadoi: 10.1109/WiSPNET.2016.7566122/ SCI/SCOPUS
- Atul Kumar Dwivedi, S. Ghosh, N. D. Londhe, Bit Level FIR Filter Optimization Using Hybrid Artificial Bee Colony Algorithm, in proceedings of 2015 Annual IEEE India Conference (INDICON Dec 2015), pp. 1–6, Delhi, India doi:10.1109/INDICON.2015.7443741,SCI/SCOPUS.
- A. K. Dwivedi, "IEEE Global early carrer faculty virtual mini-conference, September 19, 2014

- RaghvendraBhushan, Bibhudendra Acharya, Atulkumar Dwivedi, A novel data place encryption algorithm, in proceedings of IEEE conference on devices, circuits and communications, September 12,13, 2014, BIT Mesra, Ranchi, India,doi: 10.1109/APS.2014.6905245,SCI/SCOPUS.
- Atul Kumar Dwivedi, R. B. Deshmukh, R.M. Patrikar,Modeling and Simulation of Capacitive Sensor for E.Coli bacteria in water , Published in 16th International Workshop on Physics of Semiconductor Devices,IIT Kanpur, edited by Y. N. Mohapatra, B. Mazhari, M. Katiyar, Proc. of SPIE Vol. 8549, 854914 · © 2012 SPIE · CCC code: 0277-786/12/$18 · doi: 10.1117/12.927438, OCT 2012, SCI/SCOPUS.
- Sumeet Kailash Gupta, Atul Kumar Dwivedi, R. B. Deshmukh, R. M. Patrikar, "Lab on Chip for detection of E.Coli bacteria in water using capacitance modelling", in proceedings of the 2011 international comsol conference in Bangalore.
- Atul Kumar Dwivedi, R.B. Deshmukh, R.M. Patrikar, Biodetection using Capacitance modulation, in proceedings of comsol international conference in Bangalore, November, 17-19, 2010".

National conferences

- Shail bala, Atul Kumar Dwivedi, "Review of Quadrature Mirror Filter Bank Design using Evolutionary Optimization Algorithms", in souvenir of national conference on recent trends in electrical, electronics and communication, Engineering (RTEECE-19), organized by Bundelkhand Institute of Engineering and Technology, Jhansi, India, February 2019, page 16.
- Babita Sahu, Atul Kumar Dwivedi, Review and Comparative Analysis on Applications of Correlation filter, in proceedings of national conference "BITCON 2017: Recent Challenges in Electronics Engineering for national development", organized by Bhilai Institute of Technology, Durg, India, March 2017, page no 108-115.
- Atul Kumar Dwivedi "Charge based Capacitance measurement (CBCM) circuit design for Capacitive Sensors" in proceedings of national conference "URVATECH", organized by SSITM Bhilai, 17th March 2011 to 18th March 2011.
- Amol Morankar, R. M. Patrikar, Atulkumar Dwivedi, Design and simulation of MEM resonator on SOI, presented in proceedings of a national conference "RAMEMS", being organized by IIT-BHU, Varanasi to be held during 7th -9th March 2011.
- Atul KumarDwivedi,R. B. Deshmukh, R. M. Patrikar, "Microfluidic Biosensor for E. Coli in Water" presented in a national conference "RAMEMS", being organized by IIT-BHU, Varanasi to be held during 7th -9th March 2011.

- Atul Kumar Dwivedi, K. Maheshwar Rao, K. Aravind, Interference Between Bluetooth and Wireless LAN, in proceedings of National Conference organized by SSCET Bhilai(C.G.), Technovision-07, 26th October 2007 to 27th October 2007.

Reviewer positions

- Reviewer in IEEE Transactions on Circuits and Systems II: Express Briefs
- Member of editorial board in International Journal of Science & Engineering Development Research, (ISSN : 2455-2631), Member ID : 111242
- Reviewer Elsevier, Signal PRocessing
- Reviewer in Springer's Circuit Systems and Signal Processing
- Program Committee Members in sixth international conference on signal and image processing (SIP 2017),February 25-26, 2017, Sydney, Australia

Membership of Professional Bodies

- Member, Institute of Electrical and Electronics Engineers (IEEE), world's largest technical professional organization dedicated to advancing technology for the benefit of humanity. Member since: 26 March 2016, Membership no: 92525493
- Member, International Association of Engineers (IAENG), a non-profit international association for engineers and computer scientists. Member since 17 April 2018, Membership no: 213034
- Life Member, since 2019, The Indian Society for Technical Education (ISTE), a national, professional, non-profit Society registered under the Indian Societies. Member since 01 July 2019, membership number: LM 128016
- Life member Indian Science Congress Association (ISCA), a professional body under department of science & technology, ministry of science & technology, government of India, Since 12 June 2019, membership number: L38002

Awards and Accolades

- Top performing mentor award for mentees as topper/Gold medallist in recognition of the role as mentor for the NPTEL online certification course "Introduction to Programming in C" from July to December 2017
- 2nd prize in Comsol Conference Bangalore, October 29-30, 2010.
- Qualified National Eligibility Test (NET) in electronics by UGC India
- 96.29 percentile in GATE 2009.
- MHRD Scholarship during M.Tech and Ph.D.

- 3rdMerit position in 2009 batch of Electronics SSCET Bhilai.
- 1st position in school in HSSC and SSC exams.

Expert Lectures/Tutorials/Short Term Courses/ Workshops/conferences Organized

- Member Organizing Committee for ICFCCT-19 Conference, MIET College, UP
- Member, Advisory Committee for ICASAE-19, Hotel the Golden Apple, Lucknow
- Publicity Chair, International Conference on Advanced Communication & Computational Technology (ICACCT-2019) during 6-7 December 2019, at NIT Kurukshetra
- Conference Secretory, National conference on recent trends in electrical, electronics and communication engineering (RTEECE-19), during February 23-24, 2019 at BIET Jhansi
- Member, TPC, International Conference on Advanced Communication & Computational Technology (ICACCT-2019) during 6-7 December 2019, at NIT Kurukshetra
- Co-ordinator, faculty development program on "Recent advances in communication engineering" during December 26-30, 2018 in Bundelkhand Institute of Engineering and technology, Jhansi (U.P.)
- Member, media and press committee for organizing the workshop on "opportunities and challenges for holistic development of Bundelkhand region". During September 8-9, 2018 by Bundelkhand Institute of Engineering and Technology, Jhansi
- Chief faculty co-ordinator in a National level workshop on "Application of Android in designing Biped Robot", organized by Department of Electronics and Telecommunication, BIT DurgIn association with Innovation cell IIT Bombay during 10-04-2017 to 11-04-2017, number of participants 30.
- Member, organizing committee, BITCON 2017, Department of Electronics and Telecommunication, Bhilai Institute of Technology, Durg held on 28th March 2017, participants: 34(Academia), 4(Industry).
- Program Committee Members in sixth international conference on signal and image processing (SIP 2017), technically sponsored by AIRCC publishing corporation, February 25-26, 2017, Sydney, Australia
- Member program committee, 3rd Annual International workshop on Wireless Communication and Network, December 8-10, 2017, Beijing China

Projects Supervised

- Supervisor in project: Speech Steganography using least significant bit method , session 2018-2019, B. Tech, Shivi Gupta, Riya Srivas, Roopam Rajput, Bundelkhand Institute of Engineering and Technology, Jhansi (U.P.)

- Supervisor in project: Design of Pseudo Random Pattern Generator using LFSR and its application in CDMA, session 2018-2019, B. Tech, Samir Anand, Tushar Gupta, Pramod Kumar, Bundelkhand Institute of Engineering and Technology, Jhansi (U.P.)
- Supervisor in project: Variable Fractional Delay FIR Filters with low frequencies, session 2018-2019, B. Tech, Ankit Kumar, Uma, Yashanki, Bundelkhand Institute of Engineering and Technology, Jhansi (U.P.)
- Supervisor in project: Speed dialling extension for landline telephones using AVR microcontroller, session 2016-2017, Anshaj Shrivastava, Sajiv Philip, Saurabh Sharma, B. E., Chhattisgarh Swami Vivekananda Technical University, Bhilai,
- Supervisor in project: Brain Tumor Detection Using Correlation Filters, Session 2016-2017, Babita Sahu, M. Tech, Bhilai Institute of Technology Durg, Chhattisgarh Swami Vivekananda Technical University, Bhilai,
- Joint supervisor in project: Fractional order digital FIR filter design using binary cat swarm optimization, Session 2015-2016, R. Aparna, M. Tech, CSIT Durg, Chhattisgarh Swami Vivekananda Technical University, Bhilai,

Expert Lectures/Tutorial/Short Term Courses/Conference Seminars/Workshops delivered

- Expert lecture on "Image processing using MATLAB", SR group of institution, Jhansi on 2nd September, 2019.
- Expert lecture on "Optimization based Image Analysis", in national workshop on programming, simulation and optimization using MATLAB, held at NIT Raipur on 26 August 2019.
- Expert lecture on "Multi objective optimization", in Speaker in our National workshop on Programming, Simulation and Optimization using MATLAB, held at NIT Raipur on 25 August 2019.
- Keynote speech on "Evolutionary optimization in signal processing" in "IEEE International Conference on Wireless Communications Signal Processing and Networking (Wispnet 2016)", at SSN group of institutions Chennai, during March 23-25, 2016.
- Expert lecture in the workshop on "MATLAB and Simulink", at SSIPMT Raipur, held during January 20-23, 2016.
- Expert lecture in the special course on "Applications of power electronics and electrical drives in AC locomotives and LHB Coaches" organised by NIT Raipur, during June 29, 2016 to July 11, 2016.

Industrial Training/Visit

- Industrial tour of 3rd year ECE students on 22 March 2018 at P. T. P. P. (UPRVNLM), Parichha (U. P.).
- Industrial training, from 4 June 2007 to 14 July 2007 at Bhilai Steel plant, Bhilai

Expert Lectures/Tutorials/Workshops /Short Term Courses attended

- World bank TEQIP-III sponsored one week FDP on Science, Technology, Engineering and Management, organized by department of applied science, humanities and management, BIET Jhansi from October 4, 2018 to October 8, 2018.
- World bank TEQIP-III sponsored summer training program on active learning for senior faculty, organized by Indian Institute of technology Kanpur, from June 11, 2018 to June 15, 2018
- World bank TEQIP-III sponsored workshop on Applied statistics for experimenters, organized by Bundelkhand Institute of Engineering and Technology Jhansi from March 20, 2018 to March 21, 2018.
- World bank TEQIP-III sponsored one week FDP on Communication control and Networking, organized by department of electronics and communication, BIET Jhansi from February 19, 2018 to February 23, 2018.
- AICTE approved faculty development program(FDP201x) on "Pedagogy for online and blended teaching learning process" conducted by Indian Institute of technology, Bomabay from September 14, 2017 to October 12, 2017 under Pandit Madan Mohan Malviya National Mission for Teachers and Teaching, MHRD, Government of India
- AICTE approved faculty development program(FDP101x) on "Foundation Program in ICT for Education" conducted by Indian Institute of technology, Bomabay from August 3, 2017 to September 17, 2017 under Pandit Madan Mohan Malviya National Mission for Teachers and Teaching, MHRD, Government of India
- Two week course on "Introduction to data-structures and programming in C" jointly organized by E and ICT academies of IIT Guwahati, IIT Roorkee, IIITDM Jabalpur, NIT Patna, NIT Jaipur, NIT Warangal, from 01-07-2017 to 10-07-2017(10 days).
- Short term training program on "Medical Signal and Image Processing-II (MSIP 2017)" held under continuing education cell from 22-05-2017 to 27-05-2017(One week, 6 days) organized by National Institute of Technology, Raipur.
- ISTE one month Short term training program on "CMOS, Mixed Signal and Radio Frequency VLSI Design" conducted by Indian Institute of Technology, Kharagpur from 26-12-2016, to 04-02-2017. (The workshop was help under the National Mission on education through ICT (MHRD).

- National Workshop on "Scientific Paper Writing" conducted by Bhilai Institute of Technology, Durg,India on 15th October 2016.
- Short term training program on "Soft computing and intelligent techniques in science and engineering (SCI-TSE)", under TEQIP-II, organised by department of computer science and engineering, National Institute of Technology, Raipur, during 30-09-2016 to 04-10-2016 (one week).
- Short term course on, "Linear and non-linear signal processing and its application on bio-medical signals", under TEQIP-II, organised by department of bio-medical engineering, National Institute of technology, Raipur, during 9th to 11th September, 2016
- Online course on, "Writing your manuscript" by Springer author academy, during July 2016
- Course on "MATLAB programming for Numerical Computation", organized by NPTEL in partnership with MATHWORKS and NASSCOM, during Jan to March 2016
- A virtual workshop on "IEEE Early Career Faculty Development course" organized by IEEE, on December 23, 2015
- A complementary seminar on "MATLAB and Simulink for Engineering education", organized by Mathworks at the Gateway International, Raipur, during February 24, 2015
- A Workshop on "Uses of E-resources", organized by NIT Raipur, on September, 11, 2015.
- A workshop on "Academic ethics and IPR" organized by NIT Raipur and CCOST Raipur, during April 04-05, 2014
- A course on "Modelling and Simulation using MATLAB", organized by iversity global community for higher education, during April 2014 to August 2014.
- A short term course on "Advances in VLSI Signal Processing" offered by Indian Institute of Technology Kharagpur under continuing education program from December 20, 2012 to December 24, 2012 (one week).
- A workshop on "Electronic System Design and Manufacturing" organized by Ministry of communication and Bhilai Institute of Technology, Durg on August 25,2012.
- A Tutorial on "Micro-Electro-Mechanical-Systems" organized by VNIT Nagpur and RKNEC Nagpur, during September, 29, 2010
- A program "A Course in MATLAB: Programming and Modelling" conducted by Chhattisgarh training and Research organisation, Bhilai during April- May 2010.
- A Workshop on "MATLAB Applications in Engineering", organized by the institution of Engineers (I), Bhilai Chapter and SSCET Bhilai during January 19-20 2007.

Institutional Responsibilities

- Training and placement officer, Department of electronics and communication engineering, BIET Jhansi
- Warden Dr. A. P. J. Abdul Kalam Hostel, B. I. E. T. Jhansi, from 02 May 2018.
- In charge, department of electronics, BIT durg for National Institute of Ranking framework (NIRF) by ministry of human recourses, government of India in year 2016-17.
- Member, Research and Development Committee, Bhilai Institute of Technology, Durg in year 2016-17
- Member, NAAC, Criterion-1, curricular aspects committee, Bhilai Institute of Technology, Durg in year 2016-17

Departmental Responsibilities

- Officer in change, departmental Library, department of electronics and communication engineering, BIET jhansi
- Lab In charge, basic electronics lab, department of electronics engineering, Bhilai institute of technology, Durg during session 2016-2017
- In charge, NBA Criterion-7, Department of electronics and Communication Engineering, BIET Jhansi

Declaration

I hereby declare that the information given by me is true and I am solely responsible for the same.

Date:

Place: Dr. Atul Kumar Dwivedi