G.B. TECHNICAL UNIVERSITY, LUCKNOW

Syllabus

3rd & 4th Year

[Effective from session 2009-10]

1. **B.Tech. Electronics Engg.**
2. **B.Tech. Electronics & Communication Engg.**
3. **B.Tech. Electronics & Telecommunication Engg.**

U.P. TECHNICAL UNIVERSITY, LUCKNOW
Study and Evaluation Scheme
B.Tech. Electronics Engineering, B.Tech. Electronics & Communication Engg.,
B.Tech. Electronics & Telecommunication Engg.
(Also for B.Tech. Biomedical Engineering)
[Effective from the session 2009-10]

YEAR 2nd, SEMESTER-III

S. No.	Course Code	SUBJECT	PERIODS			Evaluation Scheme				Subject Total	Credit
						SESSIONAL EXAM.			ESE		
			L	T	P	CT	TA	Total			
THEORY SUBJECTS											
1.	EHU-301/ EHU-302	Industrial Psychology/ Industrial Sociology	2	0	0	15	10	25	50	75	2
2.	EAS-301/ EOE-031-EOE-038	Mathematics III/ Science based open Elective**	3	1	0	30	20	50	100	150	4
3.	EEC-301	Fundamentals of Electronics Devices	3	1	0	30	20	50	100	150	4
4.	EEC-302	Digital Electronics	3	1	0	30	20	50	100	150	4
5.	EEC-303	Electromagnetic Field Theory	3	1	0	30	20	50	100	150	4
6.	EEC-304	Fundamentals of Network Analysis & Synthesis	3	1	0	30	20	50	100	150	4
7.	EHU-111	*Human Values & Professional Ethics	2	2	0	15	10	25	50	75	-
PRACTICAL/DESIGN/DRAWING											
8	EEC-351	Electronics Engineering Lab I	0	0	2	--	20	20	30	50	1
9.	EEC-352	Digital Electronics Lab-I	0	0	2	--	20	20	30	50	1
10.	EEC-353	PCB & Electronics Workshop	0	0	2	--	10	10	15	25	1
11.	GP 301	General Proficiency	-	-	-	-	-	50	-	50	1
		Total	17	5	6	165	160	375	625	1000	26

***** Human Values & Professional Ethics will be offered as compulsory Audit Course for which passing marks are 40% in theory & 50% in aggregate. Students will be required to audit it within the period of their study. There will not be carry over facility for this course and a failure student will be required to repeat this course.

**** Science based open Elective**

EOE031/EOE041 Introduction to soft computing (Neural network, Fuzzy logic and Genetic algorithm)
EOE032/EOE042 Nano-sciences
EOE033/EOE043 Laser systems and applications
EOE034/EOE044 Space sciences
EOE035/EOE045 Polymer science and technology
EOE036/EOE046 Nuclear science
EOE037/EOE047 Material science
EOE038/EOE048 DISCRETE mathematics

U.P. TECHNICAL UNIVERSITY, LUCKNOW
Study and Evaluation Scheme
B.Tech. Electronics Engineering, B.Tech. Electronics & Communication Engg.,
B.Tech. Electronics & Tele Communication Engg.
(Also for B.Tech. Biomedical Engineering)
[Effective from the session 2009-10]

YEAR 2nd , SEMESTER-IV

S. No.	Course Code	SUBJECT	PERIODS			Evaluation Scheme				Subject Total	Credit
						SESSIONAL EXAM.			ESE		
			L	T	P	CT	TA	Total			
THEORY SUBJECTS											
1.	EHU-402/ EHU-401	Industrial Sociology/Industrial Psychology	2	0	0	15	10	25	50	75	2
2.	EOE-041-EOE-048/ EAS-401	Science based open Elective**/ Mathematics III	3	1	0	30	20	50	100	150	4
3.	EEC-401	Electronic circuits	3	1	0	30	20	50	100	150	4
4.	EEC-402	Computer Architecture & Organization	3	1	0	30	20	50	100	150	4
5.	EEC-403	Electronic Instrumentation and Measurements	3	1	0	30	20	50	100	150	4
6.	EEC-404	Signals and Systems	3	1	0	30	20	50	100	150	4
7.	EHU-111	*Human Values & Professional Ethics	2	2	0	15	10	25	50	75	-
PRACTICAL/DESIGN/DRAWING											
8.	EEC-451	Electronics Engineering lab II	0	0	2	--	20	20	30	50	1
9	EEC-452	Digital Electronics Lab II	0	0	2	--	20	20	30	50	1
10	EEC-453	Measurement lab	0	0	2	--	10	10	15	25	1
11.	GP 401	General Proficiency	-	-	-	-	-	50	-	50	1
		Total	**17**	**5**	**6**	**165**	**160**	**375**	**625**	**1000**	**26**

****Science based open Elective**

EOE031/EOE041 Introduction to soft computing (Neural network, Fuzzy logic and Genetic algorithm)
EOE032/EOE042 Nano-sciences
EOE033/EOE043 Laser systems and applications
EOE034/EOE044 Space sciences
EOE035/EOE045 Polymer science and technology
EOE036/EOE046 Nuclear science
EOE037/EOE047 Material science
EOE038/EOE048 DISCRETE mathematics

Modified & Revised

G.B. TECHNICAL UNIVERSITY, LUCKNOW

Study and Evaluation Scheme

B.Tech. Electronics Engineering, B.Tech. Electronics & Communication Engg., B.Tech. Electronics & Tele Communication Engg.

[Effective from the session 2010-11]

YEAR 3rd, SEMESTER-V

S. No.	Course Code	SUBJECT	PERIODS			Evaluation Scheme				Subject Total	Credit
						SESSIONAL EXAM.			ESE		
			L	T	P	CT	TA	Total			
THEORY SUBJECTS											
1	EEC 501	Integrated Circuits	3	1	0	30	20	50	100	150	4
2	EEC 502	Principles of Communications	3	1	0	30	20	50	100	150	4
3	EEC 503	Microprocessors	3	1	0	15	10	25	50	75	3
4	EEC 504	Antenna &Wave Propagation	3	1	0	15	10	25	50	75	3
5	EIC 501	Control Systems - I	3	1	0	30	20	50	100	150	4
6	EHU 501	Engineering and Managerial Economics	3	1	0	30	20	50	100	150	3
7	AUC 001	*Human Values & Professional Ethics	2	0	0	15	10	25	50	75	-
PRACTICAL/DESIGN/DRAWING											
8.	EEC 551	Integrated circuits Lab	0	0	2	--	20	20	30	50	1
9.	EIC 551	Control Systems Lab	0	0	2	--	20	20	30	50	1
10.	EEC 552	Communication Lab- I	0	0	2	--	20	20	30	50	1
11.	EEC 553	Microprocessors Lab	0	0	2	--	20	20	30	50	1
12.	GP 501	General Proficiency	-	-	-	-	-	50	-	50	1
		Total	**18**	**6**	**8**	**150**	**180**	**380**	**620**	**1000**	**26**

Modified

G.B. TECHNICAL UNIVERSITY, LUCKNOW

Study and Evaluation Scheme

B.Tech. Electronics Engineering, B.Tech. Electronics & Communication Engg., B.Tech. Electronics & Tele Communication Engg.

[Effective from the session 2010-11]

YEAR 3rd, SEMESTER-VI

S. No.	Course Code	SUBJECT	PERIODS			Evaluation Scheme				Subject Total	Credit
						SESSIONAL EXAM.			ESE		
			L	T	P	CT	TA	Total			
THEORY SUBJECTS											
1.	EHU 601	Industrial Management	3	0	0	30	20	50	100	150	3
2.	EEC 601	Digital communication	3	1	0	30	20	50	100	150	4
3.	EEC 602	Digital Signal Processing	3	1	0	30	20	50	100	150	4
4.	EEC 603	Microwave Engineering	3	1	0	30	20	50	100	150	4
5.	EEC 604	Introduction to Electric Drives	3	1	0	15	10	25	50	75	3
6.		Departmental Elective-I**	3	1	0	15	10	25	50	75	3
7.	AUC 001	*Human Values & Professional Ethics	2	0	0	15	10	25	50	75	-
PRACTICAL/DESIGN/DRAWING											
8.	EEC 654	Seminar	0	0	2	--	50	50	-	50	1
9.	EEC 651	Communication Lab – II	0	0	2	--	20	20	30	50	1
10.	EEC 653	CAD of Electronics Lab	0	0	2	--	20	20	30	50	1
11.	EEC 652	DSP Lab	0	0	2	--	20	20	30	50	1
12.	GP 601	General Proficiency	-	-	-	-	-	50	-	50	1
		Total	**18**	**5**	**8**	**150**	**210**	**410**	**590**	**1000**	**26**

LIST OF ELECTIVES:

Elective – I**

1. EEC 011 Analog signal processing
2. EEC 012 Data Structures
3. EEC 013 Advance Semiconductor Devices
4. EIC 601 Microcontroller

Syllabus third semester:

THEORY SUBJECTS

EEC 301 FUNDAMENTALS OF ELECTRONICS DEVICES			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	Crystal Properties and charge Carriers in Semiconductors: Elemental and compound semiconductor materials, crystal lattice structure, Bonding forces and energy bands in solids, charge carriers in semiconductors, carrier concentrations, drift of carriers in electric and magnetic fields.	1.1 to 1.2 3.1 to 3.4	8
II	Excess Carriers in Semiconductors: Optical absorption, luminescence, carrier life time and photo conductivity, diffusion of carriers.	4.1 to 4.3 and 4.4.1 to 4.4.4	8
III	Junction Properties: Equilibrium conditions, biased junctions, steady state conditions, reverse bias break down, transient and AC conditions. Metal semiconductor junctions.	5.2 to 5.5 5.7	10
IV	Transistors: Metal-semiconductor-field-effect-transistors (MESFET), Metal-insulator-semiconductor-field-effect-transistors (MISFET), Metal oxide semiconductor field effect transistor (MOSFET): Construction, Operation and characteristics of above devices. Bipolar junction transistors: Fundamentals of BJT operation, amplification with BJTs,	6.3.1 to 6.3.2, 6.4.1 to 6.4.2, 6.5.1 to 6.5.2 7.1 to 7.2	6
V	Some special devices: Photodiodes, photo detectors, solar cell, light emitting diodes, semiconductor lasers, light emitting materials. Tunnel Diode: degenerate semiconductors, IMPATT diode; The transferred electron mechanism: The GUNN diode. P-N-P-N diode, semiconductor controlled rectifier (SCR), bilateral devices: DIAC, TRIAC, IGBT.	8.1, 8.2.1, 8.2.3, 8.3, 8.4; 10.1 10.2 10.3.1, 10.3.2 11.1 to 11.3	8
Text Book: B. G. Streetman and S. Banerjee "Solid state electronics devices", 5th Edition, PHI.			
Reference Books:1. Alok Dutta, "Semiconductor Devices and circuits", Oxford University Press. 2. Donald A Neaman, "Semiconductor Physics and Devices Basic Principles" 3rd Ed TMH India.			

EEC 302 DIGITAL ELECTRONICS			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	Digital system and binary numbers: Signed binary numbers, binary codes, cyclic codes, error detecting and correcting codes, hamming codes. Floating point representation Gate-level minimization: The map method up to five variable, don't care conditions, POS simplification, NAND and NOR implementation, Quine Mc-Clusky method (Tabular method).	1.6, 1.7, 7.4 3.1 to 3.7, 3.10	8
II	Combinational Logic: Combinational circuits, analysis procedure, design procedure, binary adder-subtractor, decimal adder, binary multiplier, magnitude comparator, decoders, encoders, multiplexers	4.1 to 4.11	8
III	Synchronous Sequential logic: Sequential circuits, storage elements: latches, flip flops, analysis of clocked sequential circuits, state reduction and assignments, design procedure. Registers and counters: Shift registers, ripple counter, synchronous counter, other counters.	5.1 to 5.5, 5.7 to 5.8 6.1 to 6.5	8
IV	Memory and programmable logic: RAM, ROM, PLA, PAL. Design at the register transfer level: ASMs, design example, design with multiplexers.	7.1 to 7.3, 7.5 to 7.7 8.4, 8.5, 8.10	8
V	Asynchronous sequential logic: Analysis procedure, circuit with latches, design procedure, reduction of state and flow table, race free state assignment, hazards.	9.1 to 9.7	8
Text Book: M. Morris Mano and M. D. Ciletti, "Digital Design", 4th Edition, Pearson Education			
Reference Books: 1. Hill & Peterson, "Switching Circuit & Logic Design", Wiley.			

EEC 303 ELECTROMAGNETIC FIELD THEORY			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	Coordinate systems and transformation: Cartesian coordinates, circular cylindrical coordinates, spherical coordinates Vector calculus: Differential length, area and volume, line surface and volume integrals, del operator, gradient of a scalar, divergence of a vector and divergence theorem, curl of a vector and Stoke's theorem, Laplacian of a scalar.	2.1 to 2.4 3.1 to 3.8	6
II	Electrostatics: Electrostatic fields, Coulombs law and field intensity, Electric field due to charge distribution, Electric flux density, Gausses's Law – Maxwell's equation, Electric dipole and flux lines, energy density in electrostatic fields. Electric field in material space: Properties of materials, convection and conduction currents, conductors, polarization in dielectrics, dielectric constants, continuity equation and relaxation time, boundary condition. Electrostatic boundary value problems: Poission's and Laplace's equations, general procedures for soling Poission's or Laplace's equations, resistance and capacitance, method of images.	to 4.9 5.1 to 5.6, 5.8, 5.9 6.1, 6.2, 6.4 to 6.6	10
III	Magnetostatics: Magneto-static fields, Biot-Savart's Law, Ampere's circuit law, Maxwell's equation, application of ampere's law, magnetic flux density- Maxwell's equation, Maxwell's equation for static fields, magnetic scalar and vector potential. Magnetic forces, materials and devices: Forces due to magnetic field, magnetic torque and moment, a magnetic dipole, magnetization in materials, magnetic boundary conditions, inductors and inductances, magnetic energy.	7.1 to 7.7 8.1 to 8.9	8
IV	Waves and applications: Maxwell's equation, Faraday's Law, transformer and motional electromotive forces, displacement current, Maxwell's equation in final form. Electromagnetic wave propagation: Wave propagation in lossy dielectrics, plane waves in lossless dielectrics, plane wave in free space, plain waves in good conductors, power and the pointing vector, reflection of a plain wave in a normal incidence.	9.1 to 9.5 10.1, 10.3 to 10.8	8
V	Transmission lines: Transmission line parameters, Transmission line equations, input impedance, standing wave ratio and power, The Smith chart, Some applications of transmission lines.	11.1 to 11.6	8
Text Book: M. N. O. Sadiku, "Elements of Electromagnetics", 4th Ed, Oxford University Press.			
Reference Books: W. H. Hayt and J. A. Buck, "Electromagnetic field theory", 7th Ed., TMH.			

EEC 304 FUNDAMENTAL OF NETWORK ANALYSIS & SYNTHESIS			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	Signal analysis, complex frequency, network analysis, network synthesis General characteristics and descriptions of signals, step function and associated wave forms, The unit impulse Introduction to network analysis, network elements, initial and final conditions, step and impulse response, solution of network equations,	1.1 to 1.4 2.1 to 2.3 5.1 to 5.5	10
II	Review of Laplace transforms, poles and zeroes, initial and final value theorems, The transform circuit, Thevenin's and Norton's theorems, the system function, step and impulse responses, the convolution integral. Amplitude and phase responses. Network functions, relation between port parameters, transfer functions using two port parameters, interconnection of two ports.	7.1 to 7.5 8.1 9.1 to 9.4	8
III	Hurwitz polynomials, positive real functions. Properties of real immittance functions, synthesis of LC driving point immittances, properties of RC driving point impedances, synthesis of RC impedances or RL admittances, properties of RL impedances and RC admittances.	10.2,10.3 11.1 to 11.5	8
IV	Properties of transfer functions, zeroes of transmission, synthesis of Y_{21} and Z_{21} with 1Ω terminations.	12.1 to 12.3	6
V	Introduction to active network synthesis Active Network Synthesis	Material available on UPTU website & 8.7 (Text Book 2)	8

Text Book:

1. Franklin F. Kuo, "Network Analysis and synthesis", 2nd Edition, Wiley India Pvt Ltd.
2. Behrouz Peikari, "Fundamentals of Network Analysis & synthesis", Jaico Publishing House, 2006.

Reference Books: M. E. Van Valkenberg, "Network Analysis", 2nd Edition, Prentice Hall of India Ltd.

LABORATORY

EEC 351 ELECTRONICS ENGINEERING LAB I

Objective: To attain expertise in lab equipment handling and understanding the basic devices, their properties, characteristics in detail. Along with their practical usage in the circuit

1. **Study of lab equipments and components:** CRO, Multimeter, Function Generator, Power supply- Active, Passive Components & Bread Board.
2. **P-N Junction Diode**: Characteristics of PN Junction diode-Static and dynamic resistance measurement from graph.
3. **Applications of PN junction diode**: Half & Full wave rectifier- Measurement of Vrms, Vdc, and ripple factor-use of filter- ripple reduction (RC Filter)-Clipper & Clamper
4. **Properties of junctions** Zener diode characteristics. Heavy doping alters the reverse characteristics. Graphical measurement of forward and reverse resistance.
5. **Application of Zener diode:** Zener diode as voltage regulator. Measurement of percentage regulation by varying load resistor.
6. **Characteristic of BJT**: BJT in CB and CE configuration- Graphical measurement of h parameters from input and output characteristics. Measurement of A_v, A_I, R_o and R_i of CE amplifier with potential divider biasing.
7. **Characteristic of FET**: FET in common source configuration. Graphical measurement of its parameters gm, rd & m from input and output characteristics.
8. **Characteristic** of silicon-controlled rectifier.
9. **To plot** V-I Characteristics of DIAC.
10. **To draw** V-I characteristics of TRIAC for different values of Gate Currents.

EEC 352 DIGITAL ELECTRONICS LAB

Objective: To understand the digital logic and create various systems by using these logics.

1. Introduction to digital electronics lab- nomenclature of digital ICs, specifications, study of the data sheet, concept of V_{cc} and ground, verification of the truth tables of logic gates using TTL ICs.
2. Implementation of the given Boolean function using logic gates in both SOP and POS forms.
3. Verification of state tables of RS, JK, T and D flip-flops using NAND & NOR gates.
4. Implementation and verification of Decoder/De-multiplexer and Encoder using logic gates.
5. Implementation of 4x1 multiplexer using logic gates.
6. Implementation of 4-bit parallel adder using 7483 IC.
7. Design, and verify the 4-bit synchronous counter.
8. Design, and verify the 4-bit asynchronous counter.
9. Mini Project.

EEC 353 ELECTRONIC WORKSHOP & PCB LAB

Objective: To create interest in Hardware Technology.

1. Winding shop: Step down transformer winding of less than 5VA.
2. Soldering shop: Fabrication of DC regulated power supply
3. PCB Lab: (a) Artwork & printing of a simple PCB.
   (b) Etching & drilling of PCB.
4. Wiring & fitting shop: Fitting of power supply along with a meter in cabinet.
5. Testing of regulated power supply fabricated.

Fabricate and test the audio amplifier circuit by using above power supply

Syllabus fourth semester:

THEORY SUBJECTS

EEC 401 ELECTRONIC CIRCUITS			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	**Operational Amplifier:** Inverting and non-inverting configurations, difference amplifier, Effect of finite open loop gain and bandwidth on circuit performance, Large signal operation of op-amp.	2.2 to 2.6	8
II	**MOSFET:** Review of device structure operation and V-I characteristics. Circuits at DC, MOSFET as Amplifier and switch, Biasing in MOS amplifier circuits, small-signal operation and models, single stage MOS amplifier, MOSFET internal capacitances and high frequency model, frequency response of CS amplifier	4.3 to 4.9 and 4.11	8
III	**BJT:** Review of device structure operation and V-I characteristics, BJT circuits at DC, BJT as amplifier and switch, biasing in BJT amplifier circuit, small-signal operation and models, single stage BJT amplifier, BJT internal capacitances and high frequency model, frequency response of CE amplifier.	5.3 to 5.9	8
IV	**Differential Amplifier:** MOS differential pair, small signal operation of the MOS differential pair, BJT differential pair, other non-ideal characteristic of the Differential amplifier (DA), DA with active load.	7.1 to 7.5	9
V	**Feedback:** The general feed back structure, properties of negative feed back, the four basic feed back topologies, the series-shunt feedback amplifier, the series-series feedback amplifier, the shunt-shunt and shunt series feedback amplifier. **Oscillators:** Basic principles of sinusoidal oscillators, op-amp RC oscillator circuits, LC oscillator.	8.1 to 8.6 13.1 to 13.3	4+3
Text Book: A. S. Sedra and K. C. Smith, "Microelectronic Circuits", Oxford University Press, 5th Ed.			
Reference Books: **1.** Neamen D A, "Electronics Circuits", 3rd Ed TMH **2.** Jacob Millman and Arvin Grabel, "Microelectronics", 2nd Ed TMH			

EEC 402 COMPUTER ARCHITECTURE AND ORGANIZATION			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	Introduction to Design Methodology: System Design - System representation, Design Process, the gate level (revision), the register level components and PLD (revision), register level design The Processor Level: Processor level components, Processor level design.	2.1.1, 2.1.2, 2.1.3, 2.2.1, 2.2.2, 2.2.3 2.3.1, 2.3.2	8
II	Processor basics: CPU organization- Fundamentals , Additional features Data Representation – Basic formats, Fixed point numbers, Floating point numbers. Instruction sets – Formats, Types, Programming considerations.	3.1, 3.1.1, 3.1.2, 3.2, 3.2.1, 3.2.2, 3.2.3, 3.3.1, 3.3.2, 3.3.3	8
III	Datapath Design: Fixed point arithmetic – Addition and subtraction, Multiplication and Division, Floating point arithmetic, pipelining.	4.1.1, 4.1.2, 4.1.3, 4.2.1, 4.2.2, 4.3.1, 4.3.2	6
IV	Control Design: basic concepts – introduction, hardwired control, Micro programmed control –introduction, multiplier control unit, cpu control unit, Pipeline control- instruction pipelines, pipeline performance.	5.1.1, 5.1.2, 5.1.3, 5.2.1, 5.2.2, 5.2.3, 5.3.1, 5.3.2, 5.3.3	8
V	Memory organization: Multi level memories, Address translation, Memory allocation, Caches – Main features, Address mapping, structure vs performance, System Organisation: Communication methods- basic concepts, bus control. Introduction to 8085	6.2.1, 6.2.2, 6.2.3, 6.3.1, 6.3.1, 6.3.2, 6.3.3, 7.1.1, 7.1.2 Teachers choice	10 2
Text Book: John P Hayes "Computer Architecture and Organisation" McGraw Hilll 3rd Edition			
Reference Books: M Morris Mano, "Computer System Architecture" PHI 3rd Edition			

EEC 403 ELECTRONIC INSTRUMENTATION AND MEASUREMENTS			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	Unit, dimensions and standards: Scientific notations and metric prefixes. SI electrical units, SI temperature scales, Other unit systems, dimension and standards. Measurement Errors: Gross error, systematic error, absolute error and relative error, accuracy, precision, resolution and significant figures, Measurement error combination, basics of statistical analysis. PMMC instrument, galvanometer, DC ammeter, DC voltmeter, series ohm meter,	1.1 to 1.7 2.1 to 2.5 3.1 to 3.4	8
II	Transistor voltmeter circuits, AC electronic voltmeter, current measurement with electronic instruments, multimeter probes Digital voltmeter systems, digital multimeters, digital frequency meter system	4.1, 4.2, 4.4, 4.5, 4.7 6.1 to 6.3	8
III	Voltmeter and ammeter methods, Wheatstone bridge, low resistance measurements, low resistance measuring instruments AC bridge theory, capacitance bridges, Inductance bridges, Q meter	7.1, 7.3, 7.4, 7.5 8.2 to 8.4, 8.9	8
IV	CRO: CRT, wave form display, time base, dual trace oscilloscope, measurement of voltage, frequency and phase by CRO, Oscilloscope probes, Oscilloscope specifications and performance. Delay time based Oscilloscopes, Sampling Oscilloscope, DSO, DSO applications	9.1, 9.3, 9.4, 9.5, 9.7, 9.9, 9.12 10.1, 10.3, 10.4, 10.5	8
V	Instrument calibration: Comparison method, digital multimeters as standard instrument, calibration instrument Recorders: X-Y recorders, plotters	12.1, 12.2, 12.3 13.2, 13.4	8
Text Book: David A. Bell, "Electronic Instrumentation and Measurements", 2nd Ed., PHI , New Delhi 2008.			
Reference Books: 1. Oliver and Cage, "Electronic Measurements and Instrumentation", TMH, 2009. 2. Alan S. Morris, "Measurement and Instrumentation Principles", Elsevier (Buterworth Heinmann), 2008.			

EEC 404 SIGNALS AND SYSTEMS			3 1 0
Unit No.	Topics	Chapter/ Section	Proposed number of Lectures
I	**Signals:** Definition, types of signals and their representations: continuous-time/discrete-time, periodic/non-periodic, even/odd, energy/power, deterministic/ random, one-dimensional/multi-dimensional; commonly used signals (in continuous-time as well as in discrete-time): unit impulse, unit step, unit ramp (and their inter-relationships), exponential, rectangular pulse, sinusoidal; operations on continuous-time and discrete-time signals (including transformations of independent variables).	1.1 to 1.5	6
II	**Laplace-Transform (LT) and Z-transform (ZT):** (i) One-sided LT of some common signals, important theorems and properties of LT, inverse LT, solutions of differential equations using LT, Bilateral LT, Regions of convergence (ROC) (ii) One sided and Bilateral Z-transforms, ZT of some common signals, ROC, Properties and theorems, solution of difference equations using one-sided ZT, s- to z-plane mapping	2.1 to 2.15	3+5
III	**Fourier Transforms (FT):** (i) Definition, conditions of existence of FT, properties, magnitude and phase spectra, Some important FT theorems, Parseval's theorem, Inverse FT, relation between LT and FT (ii) Discrete time Fourier transform (DTFT), inverse DTFT, convergence, properties and theorems, Comparison between continuous time FT and DTFT	4.1 4.11; 5.1 to 5.7	6+4
IV	**Systems:** Classification, linearity, time-invariance and causality, impulse response, characterization of linear time-invariant (LTI) systems, unit sample response, convolution summation, step response of discrete time systems, stability. convolution integral, co-relations, signal energy and energy spectral density, signal power and power spectral density, properties of power spectral density,	7.1 to 7.12; 9.2, 9.6 to 9.8	8
V	**Time and frequency domain analysis of systems** Analysis of first order and second order systems, continuous-time (CT) system analysis using LT, system functions of CT systems, poles and zeros, block diagram representations; discrete-time system functions, block diagram representation, illustration of the concepts of system bandwidth and rise time through the analysis of a first order CT low pass filter	8.1-8.6; 8.8	10
Text Book: P. Ramakrishna Rao, `Signal and Systems' 2008 Ed., Tata McGraw Hill, New Delhi			
Reference Books: 1. Chi-Tsong Chen, `Signals and Systems', 3rd Ed., Oxford University Press, 2004 2. V. Oppenheim, A.S. Willsky and S. Hamid Nawab, 'Signals & System', Pearson Education, 2nd Ed., 2003.			

LABOROTARY

EEC 451 ELECTRONICS ENGINEERING LAB II

Objective -To design and implement the circuits to gain knowledge on performance of the circuit and its application.

1. **Measurement of Operational Amplifier Parameters**-Common Mode Gain, Differential Mode Gain, CMRR, Slew Rate.
2. **Applications of Op-amp**- Op-amp as summing amplifier, Difference amplifier, Integrator and differentiator
3. **Field Effect Transistors**-Single stage Common source FET amplifier –plot of gain in dB Vs frequency, measurement of, bandwidth, input impedance, maximum signal handling capacity (MSHC) of an amplifier
4. **Bipolar Transistors**- Design of single stage RC coupled amplifier –design of DC biasing circuit using potential divider arrangement –Plot of frequency Vs gain in dB. Measurement of bandwidth of an amplifier, input impedance and Maximum Signal Handling Capacity of an amplifier.
5. **Two stage Amplifier**. Plot of frequency Vs gain. Estimation of Q factor, bandwidth of an amplifier
6. **Common Collector Configuration-Emitter Follower** (using Darlington pair)-Gain and input impedance measurement of the circuit.
7. **Power Amplifiers**-Push pull amplifier in class B mode of operation –measurement of gain.
8. **Differential Amplifier** –Implementation of transistor differential amplifier .Non ideal characteristics of differential amplifier
9. **Oscillators** -Sinusoidal Oscillators- (a) Wein bridge oscillator (b) phase shift oscillator
10. **Simulation of Amplifier** circuits studied in the lab using any available simulation software and measurement of bandwidth and other parameters with the help of simulation software.

EEC 452 DIGITALLAB II

1. TTL Transfer Characteristics and TTL IC Gates.
2. CMOS Gate Transfer Characteristics.
3. Implementation of a 3-bit SIPO and SISO shift registers using flip-flops.
4. Implementation of a 3-bit PIPO and PISO shift registers using flip-flops.
5. Design of Seven segment display driver for BCD codes.
6. BCD Adders & Subtractors
7. A L U
8. 8085 Assembly Language Programming

EEC 453 MEASUREMENT LAB

1. Study of semiconductor diode voltmeter and its us as DC average responding AC voltmeter .
2. Study of L.C.R. bridge and determination of the value of the given components.
3. Study of distortion factor meter and determination of the % distortion of the given oscillator.
4. Study of the transistor tester and determination of the parameters of the given transistors.
5. Study of the following transducer (i) PT-100 trans (ii) J- type trans. (iii) K-type trans (iv) Presser trans
6. Measurement of phase difference and frequency using CRO (lissajous figure)
7. Measurement of low resistance Kelvin's double bridge.
8. Radio Receiver Measurements

Syllabus fifth semester: **THEORY SUBJECTS**

(Revised) **EEC 501 INTEGRATED CIRCUITS**			3 1 0
Unit	Topic	Chapter/ Section From Text [1]	Proposed number of Lectures
I	**Analog Integrated circuit Design: an overview:** Current Mirrors using BJT and MOSFETs, Simple current Mirror, Base current compensated current Mirror, Wilson and Improved Wilson Current Mirrors, Widlar Current source and Cascode current Mirror **The 741 IC Op-Amp:** Bias circuit, short circuit protection circuitry, the input stage, the second stage, the output stage, and device parameters; D**C** Analysis of 741: Small Signal Analysis of input stage, the second stage, the output stage; Gain, Frequency Response of 741; a Simplified Model, Slew Rate, Relationship Between f_t and SR	5.6, 6.4, 6.5 10.1-10.6	 8
II	**Linear Applications of IC op-amps:** An Overview of Op-Amp (ideal and non ideal) based Circuits V-I and I-V converters, generalized Impedance converter, simulation of inductors Filters: First and second order LP, HP, BP BS and All pass active filters, KHN, Tow-Thomas and State Variable Biquad filters; Sinusoidal oscillators	2.2-2.7 11.4, 11.7, 12.1, 12.2	 8
III	**Digital Integrated Circuit Design-An Overview**: CMOS Logic Gate Circuits**:** Basic Structure CMOS realization of Inverters, AND, OR, NAND and NOR Gates Latches and Flip flops**:** The Latch, The SR Flip-flop, CMOS Implementation of SR Flip-flops, A Simpler CMOS Implementation of the Clocked SR Flip-flop, D Flip-flop Circuits.	13.2-13.3 13.7	 8
IV	**Non-Linear applications of IC Op-amps:** Log–Anti Log Amplifiers, Precision Rectifiers, Peak Detectors, Simple and Hold Circuits, Analog Multipliers and their applications. Op-amp as a comparator, Zero crossing detector, Schmitt Trigger, Astable multivibrator, Monostable multivibrator, Generation of Triangular Waveforms	12.1, 12.4, 12.5 12.9	8
V	**D/A and A/D converters** **Integrated Circuit Timer:** The 555 Circuit, Implementing a Monostable Multivibrator Using the 555 IC, Astable Multivibrator Using the 555 IC. **Phase locked loops (PLL):** Ex-OR Gates and multipliers as phase detectors**,** Block Diagram of IC PLL, Working of PLL and Applications of PLL.	10.9-10.11 12.7 6.5 of Ref [2]	8
Text Book: [1] Sedra and Smith, "Microelectronic Circuits", 4th Edition, Oxford University Press.			
Reference Books: [2] Michael Jacob, `Applications and Design with Analog Integrated Circuits', PHI, 2nd Edn, 2006 [3] Jacob Milliman and Arvin Grabel, "Microelectronics", 2nd Edition, TMH, 2008.			

EEC 502 PRINCIPLES OF COMMUNICATIONS			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	Introduction: Overview of Communication system, Communication channels Need for modulation, Baseband and Pass band signals, Amplitude Modulation: Double side band with Carrier (DSB-C), Double side band without Carrier, Single Side Band Modulation, DSB-SC, DSB-C, SSB Modulators and Demodulators, Vestigial Side Band (VSB), Quadrature Amplitude Modulator, Radio Transmitter and Receiver.	1.1, 3.1, 3.2, 3.3, 3.4, 3.5, 3.6	10
II	Angle Modulation, Tone Modulated FM Signal, Arbitrary Modulated FM Signal, FM Modulators and Demodulators, Approximately Compatible SSB Systems, Stereophonic FM Broadcasting, Examples Based on Mat Lab.	4.1-4.6	8
III	Pulse Modulation Digital Transmission of Analog Signals: Sampling Theorem and its applications, Pulse Amplitude Modulation (PAM), Pulse Width Modulation, Pulse Position Modulation. Their generation and Demodulation, Digital Representation of Analog Signals, Pulse Code Modulation (PCM), PCM System, Issues in digital transmission: Frequency Division Multiplexing, Time Division Multiplexing ,Line Coding and their Power Spectral density, T1 Digital System, TDM Hierarchy,.	5.1-5.5	8
IV	Differential Pulse Code Modulation, Delta Modulation. Adaptive Delta Modulation, Voice Coders, Sources of Noises, Frequency domain representation of Noise, Super position of Noises, Linear filtering of Noises ,Mathematical Representation of Noise,	5.6-5.7 7.1-7.5	7
V	Noise in Amplitude Modulation: Analysis ,Signal to Noise Ratio, Figure of Merit ,Noise in Frequency Modulation: Pre emphasis ,De Emphasis and SNR Improvement, Phase Locked Loops Analog and Digital	8.1-8.3 9.1, 9.2,9.4, 9.6, 10.1-10.3	7

Text Book:

1. H. Taube, D L Schilling, Goutom Saha, "Principles of Communication", 3rd Edition, Tata McGraw-Hill Publishing Company Ltd.

Reference Books:

1. B.P. Lathi, "Modern Digital and Analog communication Systems", 3rd Edition, Oxford University Press, 2009.
1. Simon Haykin, "Communication Systems",4th Edition, Wiley India.
2. H. P. HSU & D. Mitra , "Analog and Digital Communications", 2nd Edition, Tata McGraw-Hill Publishing Company Ltd.

EEC- 503 MICROPROCESSORS			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	Introduction to Microprocessor, Microprocessor architecture and its operations, Memory, Input & output devices, Logic devices for interfacing, The 8085 MPU, Example of an 8085 based computer, Memory interfacing.	1.1, 3.1, 3.2, 3.3, 3.5, 4.1, 4.2, 4.3,	8
II	Basic interfacing concepts, Interfacing output displays, Interfacing input devices, Memory mapped I/O, Flow chart symbols, Data Transfer operations, Arithmetic operations, Logic Operations, Branch operation, Writing assembly language programs, Programming techniques: looping, counting and indexing.	5.1, 5.2, 5.3, 5.4, 6.1, 6.2, 6.3, 6.4, 6.5, 7.1	8
III	Additional data transfer and 16 bit arithmetic instruction, Arithmetic operations related to memory, Logic operation: rotate, compare, counter and time delays, Illustrative program: Hexadecimal counter, zero-to-nine, (module ten) counter, generating pulse waveforms, debugging counter and time delay, Stack, Subroutine, Restart, Conditional call and return instructions, Advance subroutine concepts, The 8085 Interrupts, 8085 vector interrupts.	7.2, 7.3, 7.4, 7.5, 8.1, 8.2, 8.3, 8.4, 8.5, 9.1, 9.2, 9.3, 9.4, 12.1, 12.2	8
IV	Program: BCD-to-Binary conversion, Binary-to-BCD conversion, BCD-to-Seven segment code converter, Binary-to-ASCII and ASCII-to-Binary code conversion, BCD Addition, BCD Subtraction, Introduction to Advance instructions and Application, Multiplication, Subtraction with carry.	10.1, 10.2, 10.3, 10.4, 10.5, 10.6, 10.7, 10.8, 10.9	8
V	8255 Programmable peripheral interface, interfacing keyboard and seven segment display, 8254 (8253) programmable interval timer, 8259A programmable interrupt controller, Direct Memory Access and 8237 DMA controller. Introduction to 8086 microprocessor: Architecture of 8086 (Pin diagram, Functional block diagram, Register organization).	15.1, 15.2, 15.4, 15.5, 15.6, 2.11*, 2.12*	8
Text Book: 1. Ramesh Gaonkar, "Microprocessor Architecture, Programming, and Applications with the 8085", 5th Edition, Penram International Publication (India) Pvt. Ltd. 2. * Douglas V. Hall, "Microprocessors and Interfacing", 2nd Edition, TMH, 2006.			
Reference Book: Kenneth L. Short, "Microprocessors and programmed Logic", 2nd Ed, Pearson Education Inc.			

EEC 504 ANTENNA AND WAVE PROPAGATION			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	**Antennas Basics** Introduction, Basic Antenna Parameters, Patterns, Beam Area (or Beam Solid Angle)ΩA, Radiation Intensity, Beam Efficiency, Directivity D and Gain G, Directivity and Resolution, Antenna Apertures, Effective Height, The radio Communication link, Fields from Oscillating Dipole, Single-to-Noise Ratio(SNR), Antenna Temperature, Antenna Impedance, Retarded Potential, Far Field due to an alternating current element, Power radiated by a current element, Field variation due to sinusoidal current distribution.	2.1 to 2.12, 2.18 to 2.20 4.3 to 4.7	8
II	**Point Sources and Their Arrays** Introduction, Point Source ,Power Theorem and its Application to an Isotropic Source, Radiation Intensity, Arrays of Two Isotropic Point Sources, Non-isotropic but Similar Point Sources and the Principle of Pattern Multiplication, Pattern Synthesis by Pattern Multiplication, Linear Arrays of n Isotropic Point Sources of Equal Amplitude and Spacing, Linear Broadside Arrays with Non-uniform Amplitude Distributions. General Considerations. **Electric Dipoles, Thin Liner Antennas and Arrays of Dipoles and Apertures** The Short Electric Dipole, The Fields of a Short Dipole, Radiation Resistance of Short Electric Dipole, Thin Linear Antenna, Radiation Resistance of λ/2 Antenna, Array of Two Driven λ/2 Elements: Broadside Case and End-Fire Case, Horizontal Antennas Above a Plane Ground, Vertical Antennas Above a Plane Ground, Yagi-Uda Antenna Design, Long-Wire Antennas, folded Dipole Antennas.	5.1 to 5.5 5.9 to 5.11, 5.13, 5.15, 6.2 to 6.6, 6.10, 6.11, 6.14 to 6.15, 6.16 to 6.17, 6.21	10
III	The Loop Antenna. Design and its Characteristic Properties, Application of Loop Antennas, Far Field Patterns of Circular Loop Antennas with Uniform Current, Slot Antennas, Horn Antennas, Helical Antennas, The Log-Periodic Antenna, Micro strip Antennas	6.23,6.24,7.4, 7.5,7.13,7.19, 8	7
IV	**Reflector Antennas** Flat Sheet Reflectors, Corner Reflectors, The Parabola-General Properties, A comparison Between Parabolic and Corner Reflectors, The Paraboloidal Reflector, Patterns of Large Circular Apertures with Uniform Illumination, Reflector Types(summarized), Feed Methods for Parabolic Reflectors, **Antenna Measurements** Introduction, Antenna Measurement ranges, Radiation pattern Measurements, Gain and Directivity Measurements, Spectrum Analyzer	9.2,9.3,9.5 to 9.9, 9.10, 10, 11.7, 14	8
V	**Ground Wave Propagation** Plane Earth Reflection, Space Wave and Surface Wave, **Space Wave Propagation** Introduction, Field Strength Relation, Effects of Imperfect Earth, Effects of Curvature of Earth, **Sky wave Propagation** Introduction structural Details of the ionosphere, Wave Propagation Mechanism, Refraction and Reflection of Sky Waves by ionosphere, Ray Path, Critical Frequency, MUF, LUF, OF, Virtual Height and Skip Distance, Relation Between MUF and the Skip Distance, Multi-Hop Propagation, Wave Characteristics	23.2 to 23.3, 24.1 to 24.4, 25.1 to 25.6, 25.8, 25.12	10
Text Book: 1- John D Krauss, Ronald J Marhefka and Ahmad S. Khan,"Antennas and Wave Propagation", Fourth Edition, Tata McGraw Hill, 2010 Special Indian Edition.			

Reference Books:

1. A .R. Harish, M. Sachidananda, "Antennas and Wave Propagation", Oxford University Press, 2009.
2. Jordan Edwards C. and Balmain, Keith G."Electromagnetic Waves and Radiating Systems", PHI.
3. A. Das, Sisir K. Das, "Microwave Engineering", Tata McGraw Hill.

Unit	**EIC 501 CONTROL SYSTEM I**	Text Book/ Chapter	Proposed number of Lectures
I	Basic Components of a control system, Feedback and its effect, types of feedback control systems. Block diagrams and signal flow graphs, Modeling of Physical systems: electrical networks, mechanical systems elements, equations of mechanical systems, sensors and encoders in control systems, DC motors in control systems.	1.1 to 1.3 3.1 to 3.2 4.1 to 4.6	8
II	State-Variable Analysis: Vector matrix representation of state equation, state transition matrix, state-transition equation, relationship between state equations and high-order differential equations, relationship between state equations and transfer functions.	5.1 to 5.6	8
III	Time domain Analysis of Control Systems: Time response of continuous data systems, typical test signals for the time response of control systems, the unit step response and time-domain specifications, Steady-State error, time response of a first order system, transient response of a prototype second order system	7.1 to 7.6	8
IV	Stability of Linear Control Systems: Bounded-input bounded-output stability-continuous data systems, zero-input and asymptotic stability of continuous data systems, methods of determining stability, Routh Hurwitz criterion.	6.1 to 6.5	8
V	Frequency Domain Analysis: *M_r (resonant peak) and ω_r (resonant frequency)* and bandwidth of the prototype Second order system, effects of adding a zero to the forward path, effects of adding a pole to the forward path, Nyquist stability criterion, relative stability: gain margin and phase margin, stability analysis with the Bode plot	9.1to 9.11	10

Text Book: B.C. Kuo & Farid Golnaraghi, "Automatic Control Systems", 8th Edition, John Wiley India, 2008.

Reference Books:

1. William A. Wolovich, "Automatic Control Systems", Oxford University Press, 2010.
2. Joseph J. Distefano III, Allen R. Stubberud, Ivan J. Williams, "Control Systems" Schaums Outlines Series, 3rd Edition, Tata McGraw Hill, Special Indian Edition 2010.
3. I. J. Nagrath & M. Gopal, "Control System Engineering", New Age International Publishers

LABOROTARY

EEC 551 INTEGRATED CIRCUITS LAB

Objective: - To design and implement the circuits to gain knowledge on performance of the circuit and its application. These circuits should also be simulated on Pspice.

1. Log and antilog amplifiers.
2. Voltage comparator and zero crossing detectors.
3. Second order filters using operational amplifier for–
    a. Low pass filter of cutoff frequency 1 KHz.
    b. High pass filter of frequency 12 KHz.
    c. Band pass filter with unit gain of pass band from 1 KHz to 12 KHz.
4. Wien bridge oscillator using operational amplifier.
5. Determine capture range; lock in range and free running frequency of PLL.
6. Voltage regulator using operational amplifier to produce output of 12V with maximum load current of 50 mA.
7. A/D and D/A convertor.
8. Voltage to current and current to voltage convertors.
9. Function generator using operational amplifier (sine, triangular & square wave)
10. Astable and monostable multivibrator using IC 555.

EEC 552 COMMUNICATION LAB-I

1. To study DSB/ SSB amplitude modulation & determine its modulation factor & power in side bands.
2. To study amplitude demodulation by linear diode detector
3. To study frequency modulation and determine its modulation factor
4. To study PLL 565 as frequency demodulator.
5. To study sampling and reconstruction of Pulse Amplitude modulation system.
6. To study the Sensitivity, Selectivity, and Fidelity characteristics of super heterodyne receiver.
7. To study Pulse Amplitude Modulation
    a. using switching method
    b. by sample and hold circuit
8. To demodulate the obtained PAM signal by 2nd order LPF.
9. To study Pulse Width Modulation and Pulse Position Modulation.
10. To plot the radiation pattern of a Dipole, Yagi-uda and calculate its beam width.
11. To plot the radiation pattern of Horn, Parabolic & helical antenna. Also calculate beam width & element current.
12. Design and implement an FM radio receiver in 88-108 MHz.

EEC 553 MICROPROCESSOR LAB

1. Write a program using 8085 Microprocessor for Decimal, Hexadecimal addition and subtraction of two Numbers.
2. Write a program using 8085 Microprocessor for addition and subtraction of two BCD numbers.
3. To perform multiplication and division of two 8 bit numbers using 8085.
4. To find the largest and smallest number in an array of data using 8085 instruction set.
5. To write a program to arrange an array of data in ascending and descending order.
6. To convert given Hexadecimal number into its equivalent ASCII number and vice versa using 8085 instruction set.
7. To write a program to initiate 8251 and to check the transmission and reception of character.
8. To interface 8253 programmable interval timer to 8085 and verify the operation of 8253 in six different modes.
9. To interface DAC with 8085 to demonstrate the generation of square, saw tooth and triangular wave.
10. Serial communication between two 8085 through RS-232 C port.

Note :-In addition, Institutes may include two more experiments based on the expertise.

EIC 551 CONTROL SYSTEM LAB

1. DC SPEED CONTROL SYSTEM
   (a) To study D.C. speed control system on open loop and close loop.
   (b) To study of Transient performance, another time signal is added at the input of control Circuit.
   (c) To study how eddy current breaking is being disturbance rejected by close and open loop.
2. DC MOTOR POSITION CONTROL
   (a) To study of potentiometer displacement constant on D.C. motor position control.
   (b) To study of D. C. position control through continuous command.
   (c) To study of D.C. position control through step command.
   (d) To study of D.C. position control through Dynamic response.
3. AC MOTOR POSITION CONTROL
   (a) To study of A.C. motor position control through continuous command.
   (b) To study of error detector on A.C. motor position control through step command.
   (c) To study of A.C. position control through dynamic response.
4. MAGNETIC AMPLIFIER
   (a) To study Input / Output characteristic of a magnetic amplifier in mode (i) Saturable Reactor, (ii) Self Saturable Reactor.
5. SYNCHRO TRANSMITTER / RECEIVER
   (a) To study of Synchro Transmitter in term of Position v/s Phase and voltage magnitude with respect to Rotor Voltage Magnitude/Phase.
   (b) To study of remote position indication system using Synchro-transmitter/receiver.
6. PID CONTROLLER
   (a) To observe open loop performance of building block and calibration of PID Controls.
   (b) To study P, PI and PID controller with type 0 system with delay.
   (c) To study P, PI and PID controller with type 1 system.
7. LEAD LAG COMPENSATOR
   (a) To study the open loop response on compensator.
   (b) Close loop transient response.
8. LINEAR SYSTEM SIMULATOR
   (a) Open loop response
   (i) Error detector with gain, (ii) Time constant, (iii) Integrator
   (b) Close loop system
   (I) First order system (II) Second order system (III) Third order system
9. Introduction to MATLAB (Control System Toolbox), Implement at least any two experiment in MATLAB.
   a. Different Toolboxes in MATLAB, Introduction to Control Systems Toolbox.
   b. Determine transpose, inverse values of given matrix.
   c. Plot the pole-zero configuration in s-plane for the given transfer function.
   d. Determine the transfer function for given closed loop system in block diagram representation.
   e. Plot unit step response of given transfer function and find peak overshoot, peak time.
   f. Plot unit step response and to find rise time and delay time.
   g. Plot locus of given transfer function, locate closed loop poles for different values of k.
   h. Plot root locus of given transfer function and to find out S, Wd, Wn at given root & to discuss stability.
   i. Plot bode plot of given transfer function.
   j. Plot bode plot of given transfer function and find gain and phase margins
   k. Plot Nyquist plot for given transfer function and to compare their relative stability
   l. Plot the Nyquist plot for given transfer function and to discuss closed loop stability, gain and phase margin.

Syllabus sixth semester: **THEORY SUBJECTS**

EEC 601 DIGITAL COMMUNICATIONS			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	Digital Data transmission, Line coding review, Pulse shaping, Scrambling, Digital receivers, Eye diagram, Digital carrier system, Method of generation and detection of coherent & non-coherent binary ASK, FSK & PSK, Differential phase shift keying, quadrature modulation techniques. (QPSK and MSK),M-ary Digital carrier Modulation.	7.1-7.10, 10.11	10
II	Concept of Probability, Random variable, Statistical averages, Correlation, Sum of Random Variables, Central Limit Theorem, Random Process, Classification of Random Processes, Power spectral density, Multiple random processes,	8.1-8.7, 9.1-9.4	8
III	Performance Analysis of Digital communication system: Optimum linear Detector for Binary polar signaling, General Binary Signaling, Coherent Receivers for Digital Carrier Modulations, Signal Space Analysis of Optimum Detection, Vector Decomposition of White Noise Random processes, General Expression for Error Probability of optimum receivers,	10.1-10.7	8
IV	Spread spectrum Communications: Frequency Hopping Spread Spectrum(FHSS) systems, Direct Sequence Spread Spectrum, Code Division Multiple Access of DSSS, Multiuser Detection, OFDM Communications	11.1-11.7,12.7	6
V	Measure of Information, Source Encoding, Error Free Communication over a Noisy Channel capacity of a discrete and Continuous Memory less channel Error Correcting codes: Hamming sphere, hamming distance and Hamming bound, relation between minimum distance and error detecting and correcting capability , Linear block codes, encoding & syndrome decoding; Cyclic codes, encoder and decoders for systematic cycle codes; convolution codes, code tree & Trellis diagram, Viterbi and sequential decoding, burst error correction, Turbo codes.	13.1-13.5, 14.1-4.4, 14.6-14.11	8

Text Book:

1. B.P. Lathi, "Modern Digital and Analog communication Systems", 4th Edition, Oxford University Press, 2010.

Reference Books:

1. H. Taub, D L Schilling, Goutom Saha, "Principles of Communication", 3rd Edition, Tata McGraw-Hill Publishing Company Ltd.
2. John G. Proakis, "Digital Communications", 4th Edition, McGraw-Hill International.
3. Simon Haykin, "Communication Systems",4th Edition, Wiley India.
4. H P HSU & D Mitra, "Analog and Digital Communications", 2nd Edition, Tata McGraw-Hill Publishing Company Ltd.

EEC 602 DIGITAL SIGNAL PROCESSING			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	**Realization of Digital Systems:** Introduction, direct form realization of IIR systems, cascade realization of an IIR systems, parallel form realization of an IIR systems, Ladder structures: continued fraction expansion of H(z), example of continued fraction, realization of a ladder structure, example of a ladder realization.	4.1, 4.5, 4.6, 4.7, 4.8	8
II	**Design of Infinite Impulse Response Digital Filters**: Introduction to Filters, Impulse Invariant Transformation, Bi-Linear Transformation, All-Pole Analog Filters: Butterworth and Chebyshev, Design of Digital Butterworth and Chebyshev Filters	5.2-5.6	8
III	**Finite Impulse Response Filter Design:** Windowing and the Rectangular Window, Other Commonly Used Windows, Examples of Filter Designs Using Windows ,The Kaiser Window	6.2-6.5	8
IV	**Discrete Fourier Transforms:** Definitions, Properties of the DFT, Circular Convolution, Linear Convolution	7.1-7.4	8
V	**Fast Fourier Transform Algorithms:** Introduction, Decimation –In Time(DIT) Algorithm, Computational Efficiency, Decimation in Frequency(DIF) Algorithm	8.1-8.4	8
Text Books: Johnny R. Johnson, "Digital Signal Processing", PHI Learning Pvt Ltd., 2009.			
Reference Books: 1. John G Prokias, Dimitris G Manolakis, "Digital Signal Processing", Pearson Education. 2. Oppenheim & Schafer, " Digital Signal Processing" PHI			

EEC 603 MICROWAVE ENGINEERING			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	Rectangular Wave Guide: Field Components, TE, TM Modes, Dominant TE_{10} mode, Field Distribution, Power, Attenuation. Circular Waveguides: TE, TM modes. Wave Velocities, Micro strip Transmission line (TL), Coupled TL, Strip TL, Coupled Strip Line, Coplanar TL, Microwave Cavities,	4.1-4-3,11.0-11.3	8
II	Scattering Matrix , Passive microwave devices: Microwave Hybrid Circuits. , Terminations, Attenuators, Phase Shifters, Directional Couplers: Two Hole directional couplers, S Matrix of a Directional coupler, Hybrid Couplers, Microwave Propagation in ferrites, Faraday Rotation, Isolators, Circulators. S parameter analysis of all components.	4.4-4.6	8
III	Microwave Tubes: Limitation of Conventional Active Devices at Microwave frequency, Two Cavity Klystron, Reflex Klystron, Magnetron, Traveling Wave Tube, Backward Wave Oscillators: Their Schematic, Principle of Operation, Performance Characteristic and their applications.	9.0-9.5, 10.0-10.2	8
IV	Solid state amplifiers and oscillators: Microwave Bipolar Transistor, Microwave tunnel diode, Microwave Field-effect Transistor, Transferred electron devices, Avalanche Transit –time devices: IMPATT Diode, TRAPPAT Diode,	5.0-5.1,5.3,6.0-6.1,7.0-7.3	10
V	Microwave Measurements: General set up of a microwave test bench, Slotted line carriage, VSWR Meter, microwave power measurements techniques, Crystal Detector, frequency measurement, wavelength measurements, Impedance and Refection coefficient, VSWR, Insertion and attenuation loss measurements, measurement of antenna characteristics, microwave link design.	14.1-14.4 (Book 2)	8

Text Books:

1. Samuel Y. Liao, "Microwave Devices and Circuits", 3rd Ed, Pearson Education.
2. A. Das and S. K. Das, "Microwave Engineering", TMH.

Reference Books:

1. R.E Collin, "Foundation for Microwave Engineering ", 2nd Ed., John Wiley India.

EEC 604 INTRODUCTION TO ELECTRIC DRIVES			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	**Thyristor:** Principles and Characteristics Gate Triggering Circuits	1.1-1.16 2.1-2.10	8
II	**Phase Controlled Rectifiers** Phase Angle Control, Single-phase Half-wave Controlled Rectifier (One quadrant), Single-phase Full-wave Controlled Rectifier (Two quadrant Converters),Performance Factors of Line-commutated Converters, The Performance Measures of Two-pulse Converters, Three phase Controlled Converters **Inverters:** Introduction Thyristor Inverter Classification, Series Inverters, Parallel Inverter, Three-phase Bridge Inverters, Three-phase Bridge Inverter with Input-circuit Commutation.	4.2 – 4.4 4.6 – 4.8 5.1 – 5.3, 5.5. 5.7-5.8	8
III	**Choppers:** Introduction, Principle of Chopper Operation, Control Strategies, step-up/Down Chopper, Jones Chopper **Cycloconverters:** Introduction, The Basic Principle of Operation, Single-phase to Single-phase Cycloconverter, Three-phase half-wave Cycloconverters, Cycloconverter Circuits for Three-phase Output	6.2 – 6.5, 6.8 7.1 – 7.5	8
IV	**Control of D.C. Drives:** Introduction, Basic Machine Equations, Breaking Modes, Schemes for D.C. Motor Speed Control, Single-phase Separately Excited Drives, Braking Operation of Rectifier Controlled Separately excited Motor, Single-phase Separately Excited Drives, Power Factor Improvement, Three-phase Separately Excited Drives, D.C. Chopper Drives	12.1 – 12.10	8
V	**Control of A.C. Drives:** Introduction, basic Principle of Operation, Squirrel-cage Rotor Design, Speed Control of Induction Motors, stator Voltage Control, Variable Frequency control, Rotor Resistance Control, Slip Power Recovery Scheme, Synchronous Motor Drives	13.1 – 13.9	8
Text Book: M.D. Singh & K. Khan chandani, "Power Electronics", Tata McGraw Hill 1998 Edition			
Reference Books: M H Rashid, "Power Electronics", 3rd Ed., Pearson Education, 2009.			

Departmental Electives I:

EEC- 011 ANALOG SIGNAL PROCESSING			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	Liner Analog Functions: Addition , Subtraction, Differentiation, Integration, Impedance Transformation and Conversion	4.1-4.5 Text book 1	8
II	AC/DC Signal Conversion: Signal Rectification, Peak and Valley Detection, rms to dc Conversion, Amplitude Demodulation	5.2-5.5 Text book 1	8
III	Other Nonlinear Analog Functions: Voltage Comparison, Voltage Limiting(Clipping), Logarithmic Amplifiers, Analog Multipliers, Analog Dividers	6.1-6.6 Text book 1	8
IV	Continuous time op-amp RC filters: Second order LP, HP, BP, Notch and AP transfer functions, Kirwin-Huelsman-Newcomb biquad, Ackerberg-Mosberg Circuits, Tow-Thomas biquad, compensated integrators, Sallenkey Circuits, Generalized convertor, GIC biquads.	4.2, 4.3, 4.4, 4.5 Text book 2	8
V	Transconductance-C filters: Transconductance cells, realization of resistors, integrators, amplifiers, summers and gyrators, first order and second order sections, Ladder design.	16.1, 16.2, 16.3, 16.4.2 Text book 2	8

Text Books:

1. Ramon Pallas-Areny, John G. Webster, "Analog Signal Processing", John Wiley& Sons
2. R. Schaumann and M. E. Valkenberg, "Design of Analog Circuits", Oxford University Press, 2001.

EEC 012 DATA STRUCTURE			3 1 0
Unit	**Topic**	Chapter/ Section	Proposed number of Lectures
I	**Introduction:** Basic Terminology, Elementary Data Organization, Algorithm, Efficiency of an Algorithm, Time and Space Complexity, Asymptotic notations: Big-Oh, time-Space trade-off, Abstract Data Types (ADT) **Arrays:** Definition, Single and Multidimensional Arrays, Representation of Arrays: Row major Order, and Column Major Order, Application of arrays, Sparse Matrices and their representations. **Linked lists:** Array Implementation and Dynamic Implementation of Singly Linked Lists, Doubly Linked List, Circularly Linked List, Operations on a Linked List, Insertion, Deletion, Traversal, Polynomial Representation and Addition, Generalized Linked List.		8
II	**Stacks:** Abstract Data Type, Primitive Stack operations: Push & Pop, Array and Linked Implementation of Stack in C, Application of stack: Prefix and Postfix Expressions, Evaluation of Postfix expression, Recursion, Tower of Hanoi Problem, Simulating Recursion, Principles of recursion, Tail recursion, Removal of recursion**.** **Queues:** Operations of Queue: Create, Add, Delete, Full and Empty, Circular queues, Array and linked implementation of queues in C, Dequeue and Priority Queue		8
III	Trees: Basic terminology, Binary Trees, Binary Tree Representation: Array Representation and Dynamic Representation, Complete Binary Tree, Algebraic Expressions, Extended Binary Trees, Array and Linked Representation of Binary trees, Tree Traversal algorithms: In-order, Pre-order and Post-order, Threaded Binary trees, Traversing Threaded Binary trees, Huffman algorithm.		8
IV	**Graphs:** Terminology, Sequential and linked Representations, of Graphs: Adjacency Matrices, Adjacency List, Adjacency Multi list, Graph Traversal: Depth First Search and Breadth first Search, Connected Component, Spanning Trees, Minimum Cost Spanning Trees: Prims and Kurskal algorithm, Transitive Closure and Shortest Path algorithm: Warshal Algorithm and Dijikstra Algorithm, Introduction to Activity Networks.		8
V	**Searching:** Sequential search, Binary search, Comparison and Analysis, Internal Sorting: Insertion Sort, selection, Bubble Sort, Quick Sort, Two Way Merge Sort, Heap Sort, Radix Sort, Practical consideration for Internal Sorting. Search Trees: Binary Search Trees (BST), Insertion and Deletion in BST, Complexity of search Algorithm, AVL trees, Introduction to m-way Search Trees, B Trees & B+ Trees Storage Management: Garbage Collection and Compaction.		8
Text Book: 1. Aaron M. Tenenbaum, Yedidyah Langsam and Moshe J. Augenstein "Data structures Using C and C++", PHI 2. Lipschutz, "Data Structures" Schaum's Outline Series, TMH			
Reference Books: 1. Horowitz and Sahani, "Fundamentals of Data Structures", Galgotia Publication			

EEC 013 ADVANCE SEMICONDUCTOR DEVICES			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	**Review of Fundamentals of Semiconductors:** Semiconductor Materials and their properties Carrier Transport in Semiconductors Excess Carriers in Semiconductor	3.1 to 3.8 4.1 to 4.9 5.1 to 5.7	10
II	**Junctions and Interfaces:** Description of p-n junction, Action, The Abrupt Junction, Example of an Abrupt Junction, The linearly graded Junction. The Ideal Diode Model, Real Diodes, Temperature Dependence of I-V Characteristics, High Level Injection Effects, Example of Diodes. Description of Breakdown Mechanism, Zener and Avalanche Breakdown in p-n Junction	6.1 to 6.4 7.1 to 7.5 8.1,8.3,8.5,8.7	8
III	**Majority Carrier Diodes:** The Tunnel Diode, The Backward Diode, The Schottkey Barrier Diode, Ohmic Contacts Heterojunctions.	10.1 to 10.5	6
IV	**Microwave Diodes**: The Varactor Diode, The p-i-n Diode, The IMPATT Diode, TRAPATT Diode, The BARITT Diode, Transferred Electron Devices **Optoelectronic Devices**: The Solar Cell, Photo detectors, Light Emitting Diodes, Semiconductor Lasers.	11.1 to 11.6 12.1 to 12.4	8
V	**Metal Semiconductor Field Effect Transistors**: Basic Types of MESFETs, Models for I-V Characteristics of Short – Channel MESFETs, High Frequency Performance, MESFETs Structures. **MOS Transistors and Charge Coupled Devices:** Basic Structures and the Operating Principle, I-V Characteristics, Short-Channel Effects, MOSFET Structures, Charge Coupled Devices.	15.4 to 15.7 16.4 to 16.9	8
Text Book: M.S. Tyagi, "Introduction To Semiconductor Materials And Devices", John Willy-India Pvt. Ltd.			
Reference Books: 1. S. M. Sze, "Physics of Semiconductor Devices", 2nd Edition, John Willy-India Pvt. Ltd. 2. B. G. Streetman and S. Banerjee, "Solid state electronics devices", 5th Edition, PHI.			

EIC 601 MICROCONTROLLER			3 1 0
Unit	Topic	Chapter/ Section	Proposed number of Lectures
I	Introduction , Microcontrollers and Embedded processors, Overview of the 8051, Inside the 8051, Addressing modes,	0.3, 1.1, 1.2, 2.1, 5.1-5.4,	6
II	Introduction to 8051 assembly programming, Assembling and running an 8051 program, The program counter and ROM space in the 8051, 8051 data types and directives, 8051 flag bits and the PSW register, 8051 register banks and stack, 8051 I/O programming, I/O bit manipulation programming.	2.2, 2.3, 2.4, 2.5, 2.6, 2.7, 4.1, 4.2	8
III	Programming the 8051 timers, Counter programming, Basics of serial communications, 8051 connection to RS-232, 8051 serial port programming assembly, 8051 interrupts, Programming timer interrupts, programming external hardware interrupts, programming the Serial communication interrupts, Interrupts priority in the 8051,	9.1, 9.2, 10.1, 10.2, 10.3, 11.1, 11.2, 11.3, 11.4, 11.5	10
IV	Interfacing with 8051: Memory address decoding 8031/ 51 interfacing with external ROM, 8051 data memory space, LCD, Keyboard, Parallel and Serial ADC, DAC interfacing, Sensor interfacing and Signal Conditioning, Stepper motor and DC motor,	14.2, 14.3, 14.4, 12.1, 12.2, 13.1, 13.2, 13.3, 17.2, 17.3,	10
V	Programming the 8255 and Interfacing, Introduction to Intel 8096 and MC68HC11 microcontroller*.	15.1, 15.2, Text Book 2: Ch. 3 & 4	6

Text Book:

1. Mazidi Ali Muhammad, Mazidi Gillispie Janice, and McKinlay Rolin D., " The 8051 Microcontroller and Embedded Systems using Assembly and C", Pearson, 2nd Edition.
2. Chhabra Bhupendra Singh, "Microcontrollers & its Applications" Dhanpat Rai Publishing Company

Reference Book:

1. Ayala Kenneth, "The 8051 Microcontroller", Cengage Learning, 3rd Edition
2. Shah Satish, " 8051 Microcontrollers MCS 51 Family and its variants", Oxford
3. Ghoshal Subrata, " 8051 Microcontroller Internals, Instructions, Programming and Interfacing" Pearson

LABOROTARY

EEC 651 COMMUNICATION LAB – II

1. To construct a triangular wave with the help of Fundamental Frequency and its Harmonic component.
2. To construct a Square wave with the help of Fundamental Frequency and its Harmonic component.
3. Study of Pulse code modulation (PCM) and its demodulation using Bread Board.
4. Study of delta modulation and demodulation and observe effect of slope overload.
5. Study of pulse data coding techniques for NRZ formats.
6. Study of Data decoding techniques for NRZ formats.
7. Study of Manchester coding and Decoding.
8. Study of Amplitude shift keying modulator and demodulator.
9. Study of Frequency shift keying modulator and demodulator.
10. Study of Phase shift keying modulator and demodulator
11 Study of single bit error detection and correction using Hamming code.
12 Measuring the input impedance and Attenuation of a given Transmission Line

EEC-652 DIGITAL SIGNAL PROCESSING LAB

1. With the help of Fourier series, make a square wave from sine wave and cosine waves. Find out coefficient values.
2. Evaluate 4 point DFT of and IDFT of $x(n) = 1, 0 \leq n \leq 3; 0$ elsewhere.
3. Implement the FIR Filters for 2 KHz cutoff frequency and 2 KHz bandwidth for band pass filter.
4. Design FIR filter using Fourier series expansion method.
5. Implement IIR low pass filter for a 4 KHz cutoff frequency and compare it the FIR filter with the same type use chirp as input signal.
6. Verify Blackman and Hamming windowing techniques for square wave as an input which window will give good results.
7. Implement the filter functions.
8. Generate DTMF sequence 1234567890*# and observe its spectrogram.
9. Generate an Amplitude Modulation having side low frequencies 1200 Hz and 800 Hz. Observe and verify the theoretical FFT characteristics with the observed ones.
10. Generate Frequency Modulation having carrier frequencies 1 KHz and modulating frequency 200 Hz with the modulation index of 0.7. Observe and verify the theoretical FFT characteristics with the observed ones.
11. Generate an FSK wave form for transmitting the digital data of the given bit sequence. Predict and verify the FFT for the same one.
12. To study the circular convolution.

EEC-553 CAD OF ELECTRONICS LAB

PSPICE Experiments

1. (a) Transient Analysis of BJT inverter using step input.
   (b)DC Analysis (VTC) of BJT inverter with and without parameters.
2. (a) Transient Analysis of NMOS inverter using step input.
   (b) Transient Analysis of NMOS inverter using pulse input.
   (c) DC Analysis (VTC) of NMOS inverter with and without parameters.
3. (a) Analysis of CMOS inverter using step input.
   (b) Transient Analysis of CMOS inverter using step input with parameters.
   (c) Transient Analysis of CMOS inverter using pulse input.
   (d) Transient Analysis of CMOS inverter using pulse input with parameters.
   (e) DC Analysis (VTC) of CMOS inverter with and without parameters.
4. Transient & DC Analysis of NOR Gate inverter.
5. Transient & DC Analysis of NAND Gate.

VHDL Experiments

1. Synthesis and simulation of Full Adder.
2. Synthesis and Simulation of Full Subtractor.
3. Synthesis and Simulation of 3 X 8 Decoder.
4. Synthesis and Simulation of 8 X 1 Multiplexer.

5. Synthesis and Simulation of 9 bit odd parity generator.
6. Synthesis and Simulation of Flip Flop (D, and T).