

World Bank (TEQIP-II) Assisted

International Conference

On

**Recent Trends in Electrical and Electronics &
Communication Engineering (RTEECE16)**

October 15-16, 2016



Organized by

Department of Electrical and Electronics &
Communication Engineering

Bundelkhand Institute of Engineering and
Technology

(An Academic Autonomous Govt. Institution)

Jhansi – 284128 (U.P.) INDIA

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VENUE

Conference Hall, BIET, Jhansi. The inaugural function is scheduled at 10:00 AM on Oct.15, 2016.

LODGING

Limiting rooms are available in the institute guest house, which can be arranged if informed well in

advanced. In case of non-availability of rooms in the guest house candidate may be required to arrange their own mode of lodging.

IMPORTANT DATES

Last date of paper submission : 30 Sept., 2016

Notification of Acceptance : 02 Oct, 2016

Camera Ready paper submission : 05 Oct, 2016

REGISTRATION FEE

Students/ Research Scholars : Rs. 1000/-

Academician : Rs.1500/-

Industry persons : Rs.2000/-

ADDRESS FOR CORRESPONDENCE

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ABOUT THE INSTITUTION

Bundelkhand Institute of Engineering & Technology, Jhansi has proved to be the most preferred destination for aspiring technologists across the country. The institute consistently attracts the finest faculty and the best of students for its Bachelor's and Master's programmes. B.I.E.T has a rich tradition of pursuing excellence and has continually re-invented itself in terms of academic programmes and research infrastructure. Students are exposed to challenging research based academics and a host of sport, cultural and organizational activities on its vibrant campus. The presence of world class

facilities, vigorous institute-industry collaboration programmes, interdisciplinary research collaborations and industrial training opportunities help students of B.I.E.T to excel and be ahead in the competitive professional environment. In the last twenty five years, B.I.E.T has produced many illustrious professionals, whose contributions at national and international levels have been significant.

ABOUT THE DEPARTMENT

Department was established in 1989 and every year a batch of 60 engineers accomplished its education. The department has an ardent ambience to channelize the effort of students and enhance their technical skills. The department Courses are composed of subjects giving extensive coverage to basic principles and exposing students to both theoretical concepts and practical aspects, special emphasis is given on Microprocessor and its support chips, Automation in Process Industries, Digital Switching transmission network, Satellite System, Processing & Data Communication Networks

ABOUT JHANSI

The institute is located 12 K.M. away from the Jhansi railway station on NH-25 (Jhansi Kanpur national highway). Jhansi city, situated between the rivers Pahunj and Betwa is a symbol of bravery, courage and self respect. It is said that in ancient times Jhansi was a part of the regions Chedi Rashtra, Jejak Bhukit, Jajhoti and Bundelkhand.