

**World Bank (TEQIP-III) Assisted**

***Faculty Development Programme***

on

**Communication, Control & Networking**

**February 19-23, 2018**



***Organized by***

**Department of Electrical, Electronics &  
Communication Engineering**

**Bundelkhand Institute of Engineering &  
Technology**

(An Academic Autonomous Govt. Institution)

**Jhansi – 284128 (U.P.) INDIA**

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## **ADVISORY COMMITTEE**

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Prof. A. K. Nigam, coordinator TEQIP-III.

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Prof. Vikram Singh Yadav, Head, Appl. Sc. Deptt..

Prof. Tarun Soota, Head, ME Deptt.

Prof. Ajay D.Hiwarkar, Head, CHE Deptt.

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Prof. Manoj Shukla, HBTU, Kanpur.

Prof. R. K. Singh, KNIT, Sultanpur.

Prof. Subodh Wariya, IET, Lucknow.

## ABOUT THE INSTITUTION

Bundelkhand Institute of Engineering & Technology, Jhansi has proved to be the most preferred destination for aspiring technologists across the country. The Institute consistently attracts the finest faculty and the best students for its Bachelor's and Master's programmes. B.I.E.T has a rich tradition of pursuing excellence and has continually re-invented itself in terms of academic programmes and research infrastructure. Students are exposed to challenging research based academics and a host of sport, cultural and organizational activities on its vibrant campus. The presence of world class facilities, vigorous Institute-Industry collaboration programmes, Interdisciplinary research collaborations and Industrial Training opportunities help students of B.I.E.T to excel and be ahead in the competitive professional environment. In the last thirty years, B.I.E.T has produced many illustrious professionals, whose contributions at National and International levels have been significant.

## ABOUT THE DEPARTMENT

Department was established in 1989 and Presently every year a batch of 60 engineers accomplished its education. The department has an ardent ambience to channelize the effort of students and enhance their technical skills. The department Courses are composed of subjects giving extensive coverage to basic principles and exposing students to both theoretical concepts and practical aspects, special emphasis is given on Microprocessor and its support chips, Automation & Control in Process Industries, Digital Switching Transmission Network, Satellite System, Processing & Data Communication Networks etc.

## ABOUT JHANSI

The institute is located 12 K.M. away from the Jhansi railway station on NH-25 (Jhansi-Kanpur National

Highway). Jhansi city, situated between the rivers Pahunj and Betwa is a symbol of bravery, courage and self-respect.

## VENUE

Electronics & Communication Engineering Department/ Conference Hall, BIET, Jhansi.

## LODGING

Limiting rooms are available in the institute guest house, which can be arranged if informed well in advanced. In case of non-availability of rooms in the guest house candidate may be required to arrange their own mode of lodging.

## IMPORTANT DATES

**Last date of Registration Form: 27<sup>th</sup> January, 2018**  
**Notification for participant: 03 February, 2018**

## REGISTRATION FEE

There is no registration fee for the faculties from AICTE approved institutions. Faculty members can apply for the registration in the prescribed format through proper channel.

## ADDRESS FOR CORRESPONDENCE

Dr. Deepak Nagaria,  
Convener  
Associate Professor, Department of Electronics and  
Communication Engineering  
Mob:9412903700, E-mail- fdpecebiet2018@gmail.com

**Department of Electronics & Communication Engineering,  
Bundelkhand Institute of Engineering & Technology, Jhansi**  
**One Week Faculty Development Programme**  
**on**  
**Communication, Control & Networking**  
**World Bank (TEQIP-III) Assisted**  
**February 19-23, 2018**

## REGISTRATION FORM

Passport  
Size  
Photograph

Name (Mr./Ms./Dr./Prof.):.....

Designation: .....

Highest Qualification:.....

Specialization:.....

Department:.....

Organization:.....

Teaching Experience:.....

Date of Birth:.....

Address for Communication:.....

Mobile No.:.....

Email:.....

Accommodation Required: Yes / No (Only for out station participant)

Date:.....

Place:.....

**Signature of the Applicant**

## CERTIFICATE

This is to certify that Mr./Ms./Dr./Prof. \_\_\_\_\_  
is a faculty of our Institute and is permitted to attend the above  
faculty development programme.

**Signature of the Head of the Institution with Seal**