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TEQIP-3

Technical Education Quality Improvement Programme

World Bank (TEQIP-III) Assisted
National Conference
on
**Recent Trends in
Electrical, Electronics &
Communication
Engineering (RTEECE)**
(Feb 23-24, 2019)

Organized by
Department of

**ELECTRONICS AND COMMUNICATION
ENGINEERING**

**Bundelkhand Institute of
Engineering & Technology,
Jhansi – 284128 (U.P.) India**

(An Academic Autonomous Govt. Institute)

SCOPE OF THE CONFERENCE

Conference aims to bring the experts of different fields of electrical and electronics engineering nationwide on a common platform to share their views, explore the resources and make interaction among them to activate the research activities. The conference will focus on the recent trends in various technologies and their wide applications including innovations in the field of

- Electrical,
- Power electronics
- Power systems
- Electronic systems,
- Electronic Devices,
- Networking
- Modern Algorithms for Electronic signals
- Nanoelectronics
- Communication,
- Digital Signal Processing,
- Biomedical engineering,
- VLSI Design,
- Artificial Intelligence
- Solar Energy etc. and
- Wide spectrum of real world applications in Electrical, Electronics and Communication Engineering

ABOUT THE INSTITUTION

Bundelkhand Institute of Engineering & Technology, Jhansi has proved to be the most preferred destination for aspiring technologists across the country. The institute consistently attracts the finest faculty and the best of students for its bachelor's and Master's programs. B.I.E.T has a rich tradition of pursuing excellence and has continually re-invented itself in terms of academic programmes and research infrastructure. Students are exposed to challenging research-based academics and a host of sport, cultural and organizational activities on its vibrant campus. The presence of world class facilities, vigorous institute-industry collaboration programmes, interdisciplinary research collaborations and industrial training opportunities help students of B.I.E.T to excel and be ahead in the competitive professional environment. In the last twenty-five years, B.I.E.T has produced many illustrious professionals, whose contributions at national and international levels have been significant. The institute has become a research Centre where Ph.D. and MTech students are involved in research activities.

ABOUT THE DEPARTMENT

Electronics and Communication Engineering Department was established in 1989 while Electrical engineering Department was started in 2006 and every year a batch of 60 engineers accomplished its education in both the departments. In 2006, M. Tech. started in 2006 in Digital Communication in EC Department. In 2015, MTech started in Bio Medical engineering in EC Department. The department has an ardent ambience to channelize the effort of students and enhance their technical skills. The department Courses are composed of subjects giving extensive coverage to basic principles and exposing students to both theoretical concepts and practical aspects, special emphasis is given on Microprocessor and its support chips, Automation in Process Industries, Digital Switching transmission network, Satellite System, Processing & Data Communication Networks.

JHANSI

Jhansi is a historic city in the Indian state of Uttar Pradesh called the Gateway to Bundelkhand, Jhansi is situated between the rivers Pahuj and Betwa. Jhansi is well connected to all other major towns in Uttar Pradesh by road and railway networks. The National Highways Development Project has supported development of Jhansi. Srinagar to Kanyakumari North-South corridor passes through Jhansi as does the East-West corridor; consequently there has been a sudden rush of infrastructure and real estate development in the city. Jhansi was adjudged the third cleanest city of Uttar Pradesh and the fastest moving city in the North Zone in Swachh Survekshan 2018 rankings.



Jhansi fort



LODGING

Limited rooms are available in the institute guest house, which can be arranged if informed well in advanced. In case of non-availability of rooms in the guest house candidate may be required to arrange their own mode of lodging.

LOCATION

The institute is located 12 K.M. away from the Jhansi railway station on NH-25 (Jhansi-Kanpur National Highway). Jhansi city, situated between the rivers Pahunj and Betwa is a symbol of bravery, courage and self-respect.

VENUE

Conference Hall,
Electronics & Communication
Engineering Department, BIET, Jhansi.



CALL FOR PAPERS

Research articles, Review papers, posters. Extended abstracts are invited for the conference. All submission will be accepted only by email to

rteece19@gmail.com

REGISTRATION

After acceptance candidates are required to register
Students/ Research Scholars: Rs. 500/-
Academician: Rs.700/-
Industry persons: Rs.1000/-

IMPORTANT DATES

- Last date of paper submission: 08 Feb. 2019
- Notification of Acceptance: 12 Feb. 2019
- Camera Ready paper submission: 15 Feb 2019

Author Registration Form

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Recent Trends in Electrical, Electronics & Communication Engineering
(Feb 23-24, 2019)

Organized by
Department of Electrical, Electronics and Communication Engineering
Bundelkhand Institute of Engineering and Technology, Jhansi (U.P.)

Name: _____

Designation: _____

Research Interest/s: _____

Organization _____

Address: _____

Category (Gen/OBC/SC/ST): _____

Mobile No.: _____

Email id: _____

Manuscript title: _____

Payment Details: (DD should be in favour of Director, BIET Jhansi
and payable at Jhansi)

Bank Draft (DD) No. _____

Date _____

Amount _____

I hereby declare that the information given above by me, is true and I
am solely responsible for the same.

Signature of the Author