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TEQIP-3  
Technical Education Quality Improvement Programme

World Bank (TEQIP-III) Assisted  
Faculty Development Programme

on

# Current Trends in Electronics & Communication Engineering

(December 24-28, 2019)

Organized by  
Department of

ELECTRONICS AND COMMUNICATION  
ENGINEERING

Bundelkhand Institute of  
Engineering & Technology,  
Jhansi – 284128 (U.P.) India

(An Academic Autonomous Govt. Institution)

## ABOUT THE INSTITUTION

Bundelkhand Institute of Engineering & Technology, Jhansi has proved to be the most preferred destination for aspiring technologists across the country. The Institute consistently attracts the finest faculty and the best students for its Bachelor's and Master's programmes. B.I.E.T has a rich tradition of pursuing excellence and has continually re-invented itself in terms of academic programmes and research infrastructure. Students are exposed to challenging research-based academics and a host of sport, cultural and organizational activities on its vibrant campus.

The presence of world class facilities, vigorous Institute-Industry collaboration programmes, Interdisciplinary research collaborations and Industrial Training opportunities help students of B.I.E.T to excel and be ahead in the competitive professional environment. In the last thirty years, B.I.E.T has produced many illustrious professionals, whose contributions at National and International levels have been significant.

## THE DEPARTMENT

Electronics and Communication Engineering Department was established in 1989 and Presently every year a batch of 60 engineers accomplished its education. The department has an ardent ambience to channelize the effort of students and enhance their technical skills. The department Courses are composed of subjects giving extensive coverage to basic principles and exposing students to both theoretical concepts and practical aspects, special emphasis is given on Microprocessor and its support chips, Automation & Control in Process Industries, Digital Switching Transmission Network, Satellite System, Processing & Data Communication Networks etc.

## LOCATION

The institute is located 12 K.M. away from the Jhansi railway station on NH-25 (Jhansi-Kanpur National Highway). Jhansi city, situated between the rivers Pahunj and Betwa is a symbol of bravery, courage and self-respect.

## HIGHLIGHTS OF THE PROGRAM

The faculty development programme intends to facilitate up-gradation of knowledge of participants in the area of Electronics & communication Engineering. The major objectives of the program are

- to acquire knowledge about current research and technological developments
- to expose the participant about recent advances in various applications, tools and associated challenges in communication engineering

## VENUE

Seminar Hall,  
Electronics & Communication  
Engineering Department, BIET, Jhansi.

## REGISTRATION

There is no registration fee for the faculties from AICTE approved institutions. Faculty members can apply for the registration in the prescribed format through proper channel.

Last date: 23 December, 2019

Confirmation date: 23 December, 2019

(Subject to approval from competent authority)

## **Registration Form**

Faculty Development Programme  
On

**Current Trends in Electronics & Communication Engineering**  
**(December 24-28, 2019)**

**Under the aegis of World Bank TEQIP-III**

1. Name:
2. Gender:
3. Category (Gen, OBC, SC, ST):
4. Physically Handicapped (Yes/No):
5. Designation:
6. Discipline/Branch:
7. Organization:
8. Higher Academic Qualification (For faculty):
9. Address of correspondence:
10. Phone No.:
11. Email:

Declaration: The Information provided is true to the best of my knowledge. If selected, I agree to abide by my rules and regulation of the course. I also agree to inform the coordinators in case, I am unable to attend the course.

Signature of the participant  
Dated:

Signature with Seal  
Head of Department/Institute