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### Patron

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Prof. D. K. Srivastava, Head ECE & EE

### Workshop Coordinator

Prof. Deepak Nagaria

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Prof. A. K. Verma, Coordinator Accreditation

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Prof. N. P. Yadav, Registrar

Prof. Mukesh Shukla, Civil Engineering,

Prof. V. S. Yadav, Applied Science and Humanities

Mr. S. K. Jain, Finance Officer

### Important Dates:

Last date of receipt of application: **17th Feb. 2020**

Notification to participants: **18th Feb. 2020**

Workshop commencement: **19-20th Feb. 2020**

**On-spot registrations are also allowed subject to availability of seats.**

### Eligible Participants

Faculty, Ph. D. Scholars

Students of M. Tech. & B. Tech. (EE & ECE Department)

## REGISTRATION FORM

### Workshop on LabVIEW & Its Application

(19<sup>th</sup> & 20<sup>th</sup> February 2020)

Under the aegis of World Bank TEQIP-3

1. Name: .....
2. Gender: .....
3. Category (Gen/ OBC/ SC/ ST): .....
4. Physically Handicapped (Yes/No): .....
5. Designation (for faculty): .....
6. Higher Academic Qualification (For faculty): .....
7. Course, Year & Semester (For students): .....
8. Discipline/ Branch:.....
9. Organization: .....
10. Address of correspondence: .....
11. Mobile No.: .....
12. Email: .....

#### Declaration:

The information provided is true to the best of my knowledge.

Date:

Place:

Signature of the Participant

Forwarding remark of the Head of the Department/Institute.

Signature with Seal  
Head of Department/Institute

### Contact Person

Prof. Deepak Nagaria: +91 9412903700

Workshop Coordinator

Email: [deepaknagariaaktu@gamil.com](mailto:deepaknagariaaktu@gamil.com)

## WORKSHOP ON LabVIEW & Its Application

Patron

Prof. V. K. Tyagi

Chairman

Prof. D. K. Srivastava

Coordinator

Prof. Deepak Nagaria

TEQIP-3  
Technical Education Quality Improvement Programme

World Bank TEQIP-III



Department of Electronics & Communication  
Engineering  
Bundelkhand Institute of Engineering &  
Technology, Jhansi (U.P.) India

## Advisory Committee

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2. Prof. S. N. Singh, VC, MMMUT, Gorakhpur
3. Prof. N. B. Singh, VC, HBTU, Kanpur
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15. Prof. A. K. Mishra, Director, REC, Ambedkar Nagar
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17. Prof. Aseem Chandel, Director, REC, Mainpuri
18. Prof. B. D. K. Patro, Director, REC, Kannauj
19. Prof. Rachna Asthana, Director, AITH, Kanpur
20. Prof. H. O. Gupta, Director, J. P. Institute, Noida
21. Prof. Dharmendera Singh, IIT, Rorkee
22. Prof. Neelam Srivastava, IET, Lucknow
23. Prof. Rajendera Nagaria, MNNIT, Prayagraj
24. Prof. Subodh Wariya, IET, Lucknow

## Workshop Experts

1. Er. Surendera Mahavir Jain, Scientist-F, Arial Delivery Research & Development Estt., Agra (Operating Agency-DRDO).
2. Er. Amit Yadav, Assistant Professor (Guest)  
Faculty of Engineering, Dayal Bagh Education Institute, Agra & Free Lancer LabVIEW Trainer.

## Workshop Highlights

Globalization, changing demographics and technological advancements are some of the key driving forces of the future. Laboratory Virtual Instrument Engineering Workbench (LabVIEW) is a system-design platform and development environment for a visual programming language from instrument. This includes extensive support for interfacing to devices, instruments and other devices. LabVIEW also includes a compiler that produces native codes for the CPU Platform. The graphical code is translated in to executable machine code by a complier. Many libraries with a large number of functions for data acquisition, signal generation, Mathematics, statistics, signal conditioning, analysis etc., along with the numerous for functions such as integration, filters, and other specialization abilities usually associated with data capture from hardware sensors is enormous. LabVIEW is an inherently concurrent language, so it is very easy to program multiple tasks that are performed in parallel via multithreading.

## Workshop Objectives

- To provide information and develop soft skills
- To understand and implement code compiler.
- To understand front panels, block diagrams, icons and connector panes.
- To be able to use the programming structures and data type exist in LabVIEW
- To use various editing and debugging Techniques
- To know about applications of LabVIEW in the field of Electronics and Electrical Engineering



## About the Institute

Bundelkhand Institute of Engineering & Technology, Jhansi (BIET Jhansi), one of the best engineering institutes of Uttar Pradesh, is a flourishing cauldron of excellence in technical education, high-quality research, community service, and expert consultancy services. Since its inception, BIET Jhansi's vision is to become one of the leading teaching and research institutions in the country. The institute is committed to meet social responsibilities and setting high standards for all our stakeholders. BIET, Jhansi has completed its glorious 31 years of existence in the year 2019. The institute is running seven B. Tech. programs viz Chemical Engineering, Civil Engineering, Computer Science & Engineering, Electrical Engineering, Electronics and Communication Engineering, Information Technology and Mechanical Engineering, five M. Tech. Programs and one MBA program.

## Location of the Institute

The Jhansi is well connected to all parts of the country by road and rail. It is en-route Delhi-Mumbai main line of Indian Railway. BIET Campus is approximately 13Km from Jhansi Railway station. The nearest Airports are Gwalior & Agra, which are well connected by road & rail.

## Registration Fee

Registration fee: **Free**

**\*Institute will not provide TA/DA to participant.**