

**Bundelkhand Institute of Engineering & Technology,
Jhansi**

**An Interdisciplinary Graduate Course
ON
BACHELOR OF TECHNOLOGY
IN
Computer Science & Engineering**



**Study and Evaluation Scheme
[Effective from the session 2019-20]**

**Bundelkhand Institute of Engineering & Technology,
Jhansi**



**Syllabus
of
B. Tech.
Computer Science & Engineering**

1st Year (I & II Semester)

STUDY & EVALUATION SCHEME

B. Tech computer science & Engineering

YEAR I

SEMESTER - I

S. No	Code	SUBJECT	PERIODS			EVALUATION SCHEME				END SEMESTER		TOTAL	CREDIT
			L	T	P	CT	TA	Total	PS	TE	PE		
3 WEEKS COMPULSORY INDUCTION PROGRAM													
1	KAS101/ KAS102	Physics/Chemistry	3	1	3	30	20	50	25	100	25	200	5.5
2	KAS103	Mathematics-I	3	1	0	30	20	50	-	100	-	150	4
3	KEE 101/ KCS101	Basic Electrical Engineering/Programming for Problem Solving	3	1	2	30	20	50	25	100	25	200	5
4	KCE101/ KWS101	Engineering Graphics & Design/Workshop Practices	1	0	4	-	-	-	25	-	25	50	3
	MOOCs (For B.Tech. Hons. Degree)*												0
	TOTAL											600	17.5

SEMESTER II

S. No.	Code	SUBJECT	PERIODS			EVALUATION SCHEME				END SEMESTER		TOTAL	CREDIT
			L	T	P	CT	TA	Total	PS	TE	PE		
1	KAS201/ KAS202	Physics/Chemistry	3	1	3	30	20	50	25	100	25	200	5.5
2	KAS203	Mathematics II	3	1	0	30	20	50	-	100	-	150	4
3	KEE201/ KCS201	Basic Electrical Engineering/Programming for Problem Solving	3	1	2	30	20	50	25	100	25	200	5
4	KCE201/ KWS201	Engineering Graphics & Design/Workshop Practices	1	0	4	-	-	-	25	-	25	50	3
5	KAS204	Professional English	2	0	2	30	20	50	-	100	-	150	3
	MOOCs (For B.Tech. Hons. Degree)*												0
	TOTAL											750	20.5

Mini Project or Internship (3-4 weeks) shall be conducted during summer break after II semester and will be assessed during III semester

*** List of MOOCs (NPTEL) Based Recommended Courses for first year B. Tech Students**

1. Developing Soft Skills and personality-Odd Semester-8 Weeks-3 Credits
2. Enhancing Soft Skills and personality-Even Semester-8 Weeks-3 Credits

*** AICTE Guidelines in Model Curriculum:**

After successful completion of 160 credits, a student shall be eligible to get Under Graduate degree in Engineering. A student will be eligible to get Under Graduate degree with Honours only, if he/she completes additional university recommended courses only (Equivalent to 20 credits; NPTEL Courses of 4 Weeks, 8 Weeks and 12 Weeks shall be of 2, 3 and 4 Credits respectively) through MOOCs. For registration to MOOCs Courses, the students shall follow NPTEL Site <http://nptel.ac.in/> as per the NPTEL policy and norms. The students can register for these courses through NPTEL directly as per the course offering in Odd/Even Semesters at NPTEL. These NPTEL courses (recommended by the University) may be cleared during the B. Tech degree program (not necessary one course in each semester). After successful completion of these MooCs courses the students, shall, provide their successful completion NPTEL status/certificates to the University (COE) through their college of study only. The student shall be awarded Hons. Degree (on successful completion of MOOCS based 20 credit) only if he/she secures 7.50 or above CGPA and passed each subject of that Degree Programme in single attempt without any grace marks.

Programming for Problem Solving

Module – 1 : (Introduction to Programming) [08]

Introduction to components of a computer system: Memory, processor, I/O Devices, storage, operating system, Concept of assembler, compiler, interpreter, loader and linker.

Idea of Algorithm: Representation of Algorithm, Flowchart, Pseudo code with examples, From algorithms to programs, source code.

Programming Basics: Structure of C program, writing and executing the first C program, Syntax and logical errors in compilation, object and executable code. Components of C language. Standard I/O in C, Fundamental data types, Variables and memory locations, Storage classes

Module – 2 : (Arithmetic expressions & Conditional Branching) [08]

Arithmetic expressions and precedence: Operators and expression using numeric and relational operators, mixed operands, type conversion, logical operators, bit operations, assignment operator, operator precedence and associativity.

Conditional Branching: Applying if and switch statements, nesting if and else, use of break and default with switch.

Module – 3 : (Loops & Functions) [08]

Iteration and loops: use of while, do while and for loops, multiple loop variables, use of break and continue statements.

Functions: Introduction, types of functions, functions with array, passing parameters to functions, call by value, call by reference, recursive functions.

Module – 4 : (Arrays & Basic Algorithms) [08]

Arrays: Array notation and representation, manipulating array elements, using multi dimensional arrays. Character arrays and strings, Structure, union, enumerated data types, Array of structures, Passing arrays to functions.

Basic Algorithms: Searching & Basic Sorting Algorithms (Bubble, Insertion and Selection), Finding roots of equations, Notion of order of complexity.

Module – 5 : (Pointer & File Handling) [08]

Pointers: Introduction, declaration, applications, Introduction to dynamic memory allocation

(malloc, calloc, realloc, free), Use of pointers in self-referential structures, notion of linked list (no implementation)

File handling: File I/O functions, Standard C preprocessors, defining and calling macros, command-line arguments.

COURSE OUTCOMES

1. To develop simple algorithms for arithmetic and logical problems.
2. To translate the algorithms to programs & execution (in C language).
3. To implement conditional branching, iteration and recursion.
4. To decompose a problem into functions and synthesize a complete program using divide and conquer approach.
5. To use arrays, pointers and structures to develop algorithms and programs.

Text books:

1. Schum's Outline of Programming with C by Byron Gottfried, McGraw-Hill
2. The C programming by Kernighan Brain W. and Ritchie Dennis M., Pearson Education.
3. Computer Basics and C Programming by V.Rajaraman , PHI Learning Pvt. Limited, 2015.
4. Computer Concepts and Programming in C, R.S. Salaria, Khanna Publishing House
5. Computer Concepts and Programming in C, E Balaguruswami, McGraw Hill
6. Computer Science- A Structured Programming Approach Using C, by Behrouz A. Forouzan, Richard F. Gilberg, Thomson, Third Edition , Cengage Learning - 2007.
7. Let Us C By Yashwant P. Kanetkar.
8. Problem Solving and Program Design in C, by Jeri R. Hanly, Elliot B. Koffman, Pearson Addison-Wesley, 2006.
9. Programming in C by Kochan Stephen G. Pearson Education – 2015.
10. Computer Concepts and Programming in C by D.S. Yadav and Rajeev Khanna, New AgeInternational Publication.
11. Computer Concepts and Programming by Anami, Angadi and Manvi, PHI Publication.
12. Computer Concepts and Programming in C by Vikas Gupta, Wiley India Publication
13. Computer Fundamentals and Programming in C. Reema Thareja, Oxford Publication
14. Problem Solving and Programming in C, R.S. Salaria, Khanna Publishing House

Programming for Problem Solving Lab

Other Reference: -

1. Use C Open Source Software referring Spoken Tutorial MOOC
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1. WAP that accepts the marks of 5 subjects and finds the sum and percentage marks obtained by the student.
2. WAP that calculates the Simple Interest and Compound Interest. The Principal, Amount, Rate of Interest and Time are entered through the keyboard.
3. WAP to calculate the area and circumference of a circle.
4. WAP that accepts the temperature in Centigrade and converts into Fahrenheit using the formula $C/5=(F-32)/9$.
5. WAP that swaps values of two variables using a third variable.
6. WAP that checks whether the two numbers entered by the user are equal or not.
7. WAP to find the greatest of three numbers.
8. WAP that finds whether a given number is even or odd.
9. WAP that tells whether a given year is a leap year or not.
10. WAP that accepts marks of five subjects and finds percentage and prints grades according to the

following criteria:

Between 90-100%-----	Print „A“
80-90%-----	Print „B“
60-80%-----	Print „C“
Below 60%-----	Print „D“

11. WAP that takes two operands and one operator from the user and perform the operation and prints the result by using Switch statement.
12. WAP to print the sum of all numbers up to a given number.
13. WAP to find the factorial of a given number.
14. WAP to print sum of even and odd numbers from 1 to N numbers.
15. WAP to print the Fibonacci series.
16. WAP to check whether the entered number is prime or not.
17. WAP to find the sum of digits of the entered number.
18. WAP to find the reverse of a number.
19. WAP to print Armstrong numbers from 1 to 100.
20. WAP to convert binary number into decimal number and vice versa.

21. WAP that simply takes elements of the array from the user and finds the sum of these elements.
22. WAP that inputs two arrays and saves sum of corresponding elements of these arrays in a third array and prints them.
23. WAP to find the minimum and maximum element of the array.
24. WAP to search an element in a array using Linear Search.
25. WAP to sort the elements of the array in ascending order using Bubble Sort technique.
26. WAP to add and multiply two matrices of order nxn.
27. WAP that finds the sum of diagonal elements of a mxn matrix.
28. WAP to implement strlen (), strcat (),strcpy () using the concept of Functions.
29. Define a structure data type TRAIN_INFO. The type contain Train No.: integer type Train name: string Departure Time: aggregate type TIME Arrival Time: aggregate type TIME Start station: string End station: string The structure type Time contains two integer members: hour and minute. Maintain a train timetable and implement the following operations:
 - (i) List all the trains (sorted according to train number) that depart from a particular section.
 - (ii) List all the trains that depart from a particular station at a particular time.
 - (iii) List all he trains that depart from a particular station within the next one hour of a given time.
 - (iv) List all the trains between a pair of start station and end station.
30. WAP to swap two elements using the concept of pointers.
31. WAP to compare the contents of two files and determine whether they are same or not.
32. WAP to check whether a given word exists in a file or not. If yes then find the number of times it occurs.

COURSE OUTCOMES

1. To write programs for arithmetic and logical problems.
2. To translate the algorithms to programs & execution (in C language).
3. To write programs for conditional branching, iteration and recursion.
4. To write programs using functions and synthesize a complete program using divide and conquer approach.
5. Write programs using arrays, pointers and structures.

**Bundelkhand Institute of Engineering & Technology,
Jhansi**



**Syllabus
of
B.Tech
Computer Science & Engineering
2nd Year (III & IV Semester)**

B.TECH (COMPUTER SCIENCE AND ENGINEERING)
2nd Year

SEMESTER- III

S. No.	Subject Codes	Subject	Periods			Evaluation Scheme				End Semester		Total	Credit
			L	T	P	CT	TA	Total	PS	TE	PE		
1	KOE031-38/ KAS302	Engineering Science Course/ Maths IV	3	1	0	30	20	50		100		150	4
2	KAS301/ KVE301	Technical Communication/	2	1	0	30	20	50		100		150	3
		Universal Human values	3	0	0								
3	KCS301	Data Structure	3	1	0	30	20	50		100		150	4
4	KCS302	Computer Organization and Architecture	3	1	0	30	20	50		100		150	4
5	KCS303	Discrete Structures & Theory of Logic	3	0	0	30	20	50		100		150	3
6	KCS351	Data Structures Using C Lab	0	0	2				25		25	50	1
7	KCS352	Computer Organization Lab	0	0	2				25		25	50	1
8	KCS353	Discrete Structure & Logic Lab	0	0	2				25		25	50	1
9	KCS354	Mini Project or Internship Assessment*	0	0	2			50				50	1
10	KNC301/ KNC302	Computer System Security/Python Programming	2	0	0	15	10	25		50			0
11		MOOCs (Essential for Hons. Degree)											
		Total										950	22

*The Mini Project or internship (3-4 weeks) conducted during summer break after II semester and will be assessed during III semester.

SEMESTER- IV

S. No.	Subject Codes	Subject	Periods			Evaluation Scheme				End Semester		Total	Credit
			L	T	P	CT	TA	Total	PS	TE	PE		
1	KAS402/ KOE041-48	Maths IV/Engg. Science Course	3	1	0	30	20	50		100		150	4
2	KVE401/ KAS301	Universal Human Values/ Technical Communication	3	0	0	30	20	50		100		150	3
			2	1	0								
3	KCS401	Operating Systems	3	0	0	30	20	50		100		150	3
4	KCS402	Theory of Automata and Formal Languages	3	1	0	30	20	50		100		150	4
5	KCS403	Microprocessor	3	1	0	30	20	50		100		150	4
6	KCS451	Operating Systems Lab	0	0	2				25		25	50	1
7	KCS452	Microprocessor Lab	0	0	2				25		25	50	1
8	KCS453	Python Language Programming Lab	0	0	2				25		25	50	1
9	KNC402/ KNC401	Python Programming/Computer System Security	2	0	0	15	10	25		50			0
10		MOOCs (Essential for Hons. Degree)											
		Total										900	21

B.TECH. (COMPUTER SCIENCE AND ENGINEERING) THIRD SEMESTER (DETAILED SYLLABUS)

DATA STRUCTURE (KCS301)		
Course Outcome (CO)		Bloom's Knowledge Level (KL)
At the end of course , the student will be able to understand		
CO 1	Describe how arrays, linked lists, stacks, queues, trees, and graphs are represented in memory, used by the algorithms and their common applications.	K ₁ , K ₂
CO 2	Discuss the computational efficiency of the sorting and searching algorithms.	K ₂
CO 3	Implementation of Trees and Graphs and perform various operations on these data structure.	K ₃
CO 4	Understanding the concept of recursion, application of recursion and its implementation and removal of recursion.	K ₄
CO 5	Identify the alternative implementations of data structures with respect to its performance to solve a real world problem.	K ₅ , K ₆
DETAILED SYLLABUS		3-1-0
Unit	Topic	Proposed Lecture
I	<p>Introduction: Basic Terminology, Elementary Data Organization, Built in Data Types in C. Algorithm, Efficiency of an Algorithm, Time and Space Complexity, Asymptotic notations: Big Oh, Big Theta and Big Omega, Time-Space trade-off. Abstract Data Types (ADT)</p> <p>Arrays: Definition, Single and Multidimensional Arrays, Representation of Arrays: Row Major Order, and Column Major Order, Derivation of Index Formulae for 1-D,2-D,3-D and n-D Array Application of arrays, Sparse Matrices and their representations.</p> <p>Linked lists: Array Implementation and Pointer Implementation of Singly Linked Lists, Doubly Linked List, Circularly Linked List, Operations on a Linked List. Insertion, Deletion, Traversal, Polynomial Representation and Addition Subtraction & Multiplications of Single variable & Two variables Polynomial.</p>	08
II	<p>Stacks: Abstract Data Type, Primitive Stack operations: Push & Pop, Array and Linked Implementation of Stack in C, Application of stack: Prefix and Postfix Expressions, Evaluation of postfix expression, Iteration and Recursion- Principles of recursion, Tail recursion, Removal of recursion Problem solving using iteration and recursion with examples such as binary search, Fibonacci numbers, and Hanoi towers. Tradeoffs between iteration and recursion.</p> <p>Queues: Operations on Queue: Create, Add, Delete, Full and Empty, Circular queues, Array and linked implementation of queues in C, Dequeue and Priority Queue.</p>	08
III	Searching: Concept of Searching, Sequential search, Index Sequential Search, Binary Search. Concept of Hashing & Collision resolution Techniques used in Hashing. Sorting: Insertion Sort, Selection, Bubble Sort, Quick Sort, Merge Sort, Heap Sort and Radix Sort.	08
IV	Graphs: Terminology used with Graph, Data Structure for Graph Representations: Adjacency Matrices, Adjacency List, Adjacency. Graph Traversal: Depth First Search and Breadth First Search, Connected Component, Spanning Trees, Minimum Cost Spanning Trees: Prims and Kruskal algorithm. Transitive Closure and Shortest Path algorithm: Warshal Algorithm and Dijkstra Algorithm.	08

V	Trees: Basic terminology used with Tree, Binary Trees, Binary Tree Representation: Array Representation and Pointer(Linked List) Representation, Binary Search Tree, Strictly Binary Tree, Complete Binary Tree . A Extended Binary Trees, Tree Traversal algorithms: Inorder, Preorder and Postorder, Constructing Binary Tree from given Tree Traversal, Operation of Insertation , Deletion, Searching & Modification of data in Binary Search . Threaded Binary trees, Traversing Threaded Binary trees. Huffman coding using Binary Tree. Concept & Basic Operations for AVL Tree , B Tree & Binary Heaps	08
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Text books:

1. Aaron M. Tenenbaum, Yedidyah Langsam and Moshe J. Augenstein, "Data Structures Using C and C++", PHI Learning Private Limited, Delhi India
2. Horowitz and Sahani, "Fundamentals of Data Structures", Galgotia Publications Pvt Ltd Delhi India.
3. Lipschutz, "Data Structures" Schaum's Outline Series, Tata McGraw-hill Education (India) Pvt. Ltd.
4. Thareja, "Data Structure Using C" Oxford Higher Education.
5. AK Sharma, "Data Structure Using C", Pearson Education India.
6. Rajesh K. Shukla, "Data Structure Using C and C++" Wiley Dreamtech Publication.
7. Michael T. Goodrich, Roberto Tamassia, David M. Mount "Data Structures and Algorithms in C++", Wiley India.
8. P. S. Deshpandey, "C and Data structure", Wiley Dreamtech Publication.
9. R. Kruse et al, "Data Structures and Program Design in C", Pearson Education.
10. Berztiss, AT: Data structures, Theory and Practice, Academic Press.
11. Jean Paul Trembley and Paul G. Sorenson, "An Introduction to Data Structures with applications", McGraw Hill.
12. Adam Drozdek "Data Structures and Algorithm in Java", Cengage Learning

Computer Organization and Architecture (KCS302)		
Course Outcome (CO)		Bloom's Knowledge Level (KL)
At the end of course , the student will be able to understand		
CO 1	Study of the basic structure and operation of a digital computer system.	K ₁ , K ₂
CO 2	Analysis of the design of arithmetic & logic unit and understanding of the fixed point and floating-point arithmetic operations.	K ₂ , K ₄
CO 3	Implementation of control unit techniques and the concept of Pipelining	K ₃
CO 4	Understanding the hierarchical memory system, cache memories and virtual memory	K ₂
CO 5	Understanding the different ways of communicating with I/O devices and standard I/O interfaces	K ₂ , K ₄
DETAILED SYLLABUS		3-1-0
Unit	Topic	Proposed Lecture
I	Introduction: Functional units of digital system and their interconnections, buses, bus architecture, types of buses and bus arbitration. Register, bus and memory transfer. Processor organization, general registers organization, stack organization and addressing modes.	08
II	Arithmetic and logic unit: Look ahead carries adders. Multiplication: Signed operand multiplication, Booths algorithm and array multiplier. Division and logic operations. Floating point arithmetic operation, Arithmetic & logic unit design. IEEE Standard for Floating Point Numbers	08
III	Control Unit: Instruction types, formats, instruction cycles and sub cycles (fetch and execute etc), micro operations, execution of a complete instruction. Program Control, Reduced Instruction Set Computer, Pipelining. Hardwire and micro programmed control: micro programme sequencing, concept of horizontal and vertical microprogramming.	08
IV	Memory: Basic concept and hierarchy, semiconductor RAM memories, 2D & 2 1/2D memory organization. ROM memories. Cache memories: concept and design issues & performance, address mapping and replacement Auxiliary memories: magnetic disk, magnetic tape and optical disks Virtual memory: concept implementation.	08
V	Input / Output: Peripheral devices, I/O interface, I/O ports, Interrupts: interrupt hardware, types of interrupts and exceptions. Modes of Data Transfer: Programmed I/O, interrupt initiated I/O and Direct Memory Access., I/O channels and processors. Serial Communication: Synchronous & asynchronous communication, standard communication interfaces.	08
Text books:		
<ol style="list-style-type: none"> 1. Computer System Architecture - M. Mano 2. Carl Hamacher, Zvonko Vranesic, Safwat Zaky Computer Organization, McGraw-Hill, Fifth Edition, Reprint 2012 3. John P. Hayes, Computer Architecture and Organization, Tata McGraw Hill, Third Edition, 1998. Reference books 4. William Stallings, Computer Organization and Architecture-Designing for Performance, Pearson Education, Seventh edition, 2006. 		

5. Behrooz Parahami, "Computer Architecture", Oxford University Press, Eighth Impression, 2011.
6. David A. Patterson and John L. Hennessy, "Computer Architecture-A Quantitative Approach", Elsevier, a division of reed India Private Limited, Fifth edition, 2012
7. Structured Computer Organization, Tannenbaum(PHI)

Discrete Structures & Theory of Logic (KCS303)		
Course Outcome (CO)		Bloom's Knowledge Level (KL)
At the end of course , the student will be able to understand		
CO 1	Write an argument using logical notation and determine if the argument is or is not valid.	K ₃ , K ₄
CO 2	Understand the basic principles of sets and operations in sets.	K ₁ , K ₂
CO 3	Demonstrate an understanding of relations and functions and be able to determine their properties.	K ₃
CO 4	Demonstrate different traversal methods for trees and graphs.	K ₁ , K ₄
CO 5	Model problems in Computer Science using graphs and trees.	K ₂ , K ₆
DETAILED SYLLABUS		3-0-0
Unit	Topic	Proposed Lecture
I	Set Theory: Introduction, Combination of sets, Multisets, Ordered pairs. Proofs of some general identities on sets. Relations: Definition, Operations on relations, Properties of relations, Composite Relations, Equality of relations, Recursive definition of relation, Order of relations. Functions: Definition, Classification of functions, Operations on functions, Recursively defined functions. Growth of Functions. Natural Numbers: Introduction, Mathematical Induction, Variants of Induction, Induction with Non-zero Base cases. Proof Methods, Proof by counter – example, Proof by contradiction.	08
II	Algebraic Structures: Definition, Groups, Subgroups and order, Cyclic Groups, Cosets, Lagrange's theorem, Normal Subgroups, Permutation and Symmetric groups, Group Homomorphisms, Definition and elementary properties of Rings and Fields.	08
III	Lattices: Definition, Properties of lattices – Bounded, Complemented, Modular and Complete lattice. Boolean Algebra: Introduction, Axioms and Theorems of Boolean algebra, Algebraic manipulation of Boolean expressions. Simplification of Boolean Functions, Karnaugh maps, Logic gates, Digital circuits and Boolean algebra.	08
IV	Propositional Logic: Proposition, well formed formula, Truth tables, Tautology, Satisfiability, Contradiction, Algebra of proposition, Theory of Inference. (8) Predicate Logic: First order predicate, well formed formula of predicate, quantifiers, Inference theory of predicate logic.	08
V	Trees: Definition, Binary tree, Binary tree traversal, Binary search tree. Graphs: Definition and terminology, Representation of graphs, Multigraphs, Bipartite graphs, Planar graphs, Isomorphism and Homeomorphism of graphs, Euler and Hamiltonian paths, Graph coloring, Recurrence Relation & Generating function: Recursive definition of functions, Recursive algorithms,	08

Method of solving recurrences.	
Combinatorics: Introduction, Counting Techniques, Pigeonhole Principle	

Text books:

1. Koshy, Discrete Structures, Elsevier Pub. 2008 Kenneth H. Rosen, Discrete Mathematics and Its Applications, 6/e, McGraw-Hill, 2006.
2. B. Kolman, R.C. Busby, and S.C. Ross, Discrete Mathematical Structures, 5/e, Prentice Hall, 2004.
3. E.R. Scheinerman, Mathematics: A Discrete Introduction, Brooks/Cole, 2000.
4. R.P. Grimaldi, Discrete and Combinatorial Mathematics, 5/e, Addison Wesley, 2004 5. Liptschütz, Seymour, “Discrete Mathematics”, McGraw Hill.
5. Trembley, J.P & R. Manohar, “Discrete Mathematical Structure with Application to Computer Science”, McGraw Hill.
6. Deo, Narsingh, “Graph Theory With application to Engineering and Computer Science.”, PHI.
7. Krishnamurthy, V., “Combinatorics Theory & Application”, East-West Press Pvt. Ltd., New Delhi

Data Structure using C Lab (KCS351)

Write C Programs to illustrate the concept of the following:

- 1 Sorting Algorithms-Non-Recursive.
- 2 Sorting Algorithms-Recursive.
- 3 Searching Algorithm.
- 4 Implementation of Stack using Array.
- 5 Implementation of Queue using Array.
- 6 Implementation of Circular Queue using Array.
- 7 Implementation of Stack using Linked List.
- 8 Implementation of Queue using Linked List.
- 9 Implementation of Circular Queue using Linked List.
- 10 Implementation of Tree Structures, Binary Tree, Tree Traversal, Binary Search Tree, Insertion and Deletion in BST.
- 11 Graph Implementation, BFS, DFS, Minimum cost spanning tree, shortest path algorithm.

Computer Organization Lab (KCS352)

1. Implementing HALF ADDER, FULL ADDER using basic logic gates
2. Implementing Binary -to -Gray, Gray -to -Binary code conversions.
3. Implementing 3-8 lineDECODER.
4. Implementing 4x1 and 8x1 MULTIPLEXERS.
5. Verify the excitation tables of various FLIP-FLOPS.
6. Design of an 8-bit Input/ Output system with four 8-bit Internal Registers.
7. Design of an 8-bit ARITHMETIC LOGIC UNIT.
8. Design the data path of a computer from its register transfer language description.
9. Design the control unit of a computer using either hardwiring or microprogramming based on its register transfer language description.
10. Implement a simple instruction set computer with a control unit and a data path.

Discrete Structure & Logic Lab (KCS353)

Programming Language/Tool Used: C and Mapple

1. Write a program in C to create two sets and perform the Union operation on sets.
2. Write a program in C to create two sets and perform the Intersection operation on sets.
3. Write a program in C to create two sets and perform the Difference operation on sets.
4. Write a program in C to create two sets and perform the Symmetric Difference operation.
5. Write a program in C to perform the Power Set operation on a set.
6. Write a program in C to Display the Boolean Truth Table for AND, OR, NOT.
7. Write a C Program to find Cartesian Product of two sets
8. Write a program in C for minimum cost spanning tree.
9. Write a program in C for finding shortest path in a Graph.

Note: Understanding of mathematical computation software Mapple to experiment the followings (exp. 10 to 25):

10. Working of Computation software
11. Discover a closed formula for a given recursive sequence vice-versa
12. Recursion and Induction
13. Practice of various set operations
14. Counting
15. Combinatorial equivalence
16. Permutations and combinations
17. Difference between structures, permutations and sets
18. Implementation of a recursive counting technique
19. The Birthday problem
20. Poker Hands problem
21. Baseball best-of-5 series: Experimental probabilities
22. Baseball: Binomial Probability
23. Expected Value Problems
24. Basketball: One and One
25. Binary Relations: Influence

B.TECH. (COMPUTER SCIENCE AND ENGINEERING) FOURTH SEMESTER
(DETAILED SYLLABUS)

Operating systems (KCS401)		
Course Outcome (CO)		Bloom's Knowledge Level (KL)
At the end of course , the student will be able to understand		
CO 1	Understand the structure and functions of OS	K ₁ , K ₂
CO 2	Learn about Processes, Threads and Scheduling algorithms.	K ₁ , K ₂
CO 3	Understand the principles of concurrency and Deadlocks	K ₂
CO 4	Learn various memory management scheme	K ₂
CO 5	Study I/O management and File systems.	K ₂ , K ₄
DETAILED SYLLABUS		3-0-0
Unit	Topic	Proposed Lecture
I	Introduction : Operating system and functions, Classification of Operating systems- Batch, Interactive, Time sharing, Real Time System, Multiprocessor Systems, Multiuser Systems, Multiprocess Systems, Multithreaded Systems, Operating System Structure- Layered structure, System Components, Operating System services, Reentrant Kernels, Monolithic and Microkernel Systems.	08
II	Concurrent Processes: Process Concept, Principle of Concurrency, Producer / Consumer Problem, Mutual Exclusion, Critical Section Problem, Dekker's solution, Peterson's solution, Semaphores, Test and Set operation; Classical Problem in Concurrency- Dining Philosopher Problem, Sleeping Barber Problem; Inter Process Communication models and Schemes, Process generation.	08
III	CPU Scheduling: Scheduling Concepts, Performance Criteria, Process States, Process Transition Diagram, Schedulers, Process Control Block (PCB), Process address space, Process identification information, Threads and their management, Scheduling Algorithms, Multiprocessor Scheduling. Deadlock: System model, Deadlock characterization, Prevention, Avoidance and detection, Recovery from deadlock.	08
IV	Memory Management: Basic bare machine, Resident monitor, Multiprogramming with fixed partitions, Multiprogramming with variable partitions, Protection schemes, Paging, Segmentation, Paged segmentation, Virtual memory concepts, Demand paging, Performance of demand paging, Pagereplacement algorithms, Thrashing, Cachelmemoryorganization, Localityofreference.	08
V	I/O Management and Disk Scheduling: I/O devices, and I/O subsystems, I/O buffering, Disk storage and disk scheduling, RAID. File System: File concept, File organization and access mechanism, File directories, and File sharing, File system implementation issues, File system protection and security.	08

Text books:

- 1 Silberschatz, Galvin and Gagne, “Operating Systems Concepts”, Wiley
- 2 Sibsankar Halder and Alex A Aravind, “Operating Systems”, Pearson Education
- 3 Harvey M Dietel, “ An Introduction to Operating System”, Pearson Education
- 4 D M Dhamdhare, “Operating Systems : A Concept based Approach”, 2nd Edition,
- 5 TMH 5. William Stallings, “Operating Systems: Internals and Design Principles ”, 6th Edition, Pearson Education

Theory of Automata and Formal Languages (KCS402)		
Course Outcome (CO)		Bloom's Knowledge Level (KL)
At the end of course , the student will be able to understand		
CO 1	Analyse and design finite automata, pushdown automata, Turing machines, formal languages, and grammars	K ₄ , K ₆
CO 2	Analyse and design, Turing machines, formal languages, and grammars	K ₄ , K ₆
CO 3	Demonstrate the understanding of key notions, such as algorithm, computability, decidability, and complexity through problem solving	K ₁ , K ₅
CO 4	Prove the basic results of the Theory of Computation.	K ₂ , K ₃
CO 5	State and explain the relevance of the Church-Turing thesis.	K ₁ , K ₅
DETAILED SYLLABUS		3-1-0
Unit	Topic	Proposed Lecture
I	Basic Concepts and Automata Theory: Introduction to Theory of Computation- Automata, Computability and Complexity, Alphabet, Symbol, String, Formal Languages, Deterministic Finite Automaton (DFA)- Definition, Representation, Acceptability of a String and Language, Non Deterministic Finite Automaton (NFA), Equivalence of DFA and NFA, NFA with ϵ -Transition, Equivalence of NFA's with and without ϵ -Transition, Finite Automata with output- Moore Machine, Mealy Machine, Equivalence of Moore and Mealy Machine, Minimization of Finite Automata, Myhill-Nerode Theorem, Simulation of DFA and NFA	08
II	Regular Expressions and Languages: Regular Expressions, Transition Graph, Kleen's Theorem, Finite Automata and Regular Expression- Arden's theorem, Algebraic Method Using Arden's Theorem, Regular and Non-Regular Languages- Closure properties of Regular Languages, Pigeonhole Principle, Pumping Lemma, Application of Pumping Lemma, Decidability- Decision properties, Finite Automata and Regular Languages, Regular Languages and Computers, Simulation of Transition Graph and Regular language.	08
III	Regular and Non-Regular Grammars: Context Free Grammar(CFG)-Definition, Derivations, Languages, Derivation Trees and Ambiguity, Regular Grammars-Right Linear and Left Linear grammars, Conversion of FA into CFG and Regular grammar into FA, Simplification of CFG, Normal Forms- Chomsky Normal Form(CNF), Greibach Normal Form (GNF), Chomsky Hierarchy, Programming problems based on the properties of CFGs.	08
IV	Push Down Automata and Properties of Context Free Languages: Nondeterministic Pushdown Automata(NPDA)- Definition, Moves, A Language Accepted by NPDA, Deterministic Pushdown Automata(DPDA) and Deterministic Context free Languages(DCFL), Pushdown Automata for Context Free Languages, Context Free grammars for Pushdown Automata, Two stack Pushdown Automata, Pumping Lemma for CFL, Closure properties of CFL, Decision Problems of CFL, Programming problems based on the properties of CFLs.	08

V	Turing Machines and Recursive Function Theory : Basic Turing Machine Model, Representation of Turing Machines, Language Acceptability of Turing Machines, Techniques for Turing Machine Construction, Modifications of Turing Machine, Turing Machine as Computer of Integer Functions, Universal Turing machine, Linear Bounded Automata, Church's Thesis, Recursive and Recursively Enumerable language, Halting Problem, Post's Correspondence Problem, Introduction to Recursive Function Theory.	08
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Text books:

- 1 Introduction to Automata theory, Languages and Computation, J.E.Hopcraft, R.Motwani, and Ullman. 2nd edition, Pearson Education Asia
- 2 Introduction to languages and the theory of computation, J Martin, 3rd Edition, Tata McGraw Hill
- 3 Elements and Theory of Computation, C Papadimitrou and C. L. Lewis, PHI
- 4 Mathematical Foundation of Computer Science, Y.N.Singh, New Age International

Microprocessor (KCS403)		
Course Outcome (CO)		Bloom's Knowledge Level (KL)
At the end of course , the student will be able to understand		
CO 1	Apply a basic concept of digital fundamentals to Microprocessor based personal computer system.	K ₃ , K ₄
CO 2	Analyze a detailed s/w & h/w structure of the Microprocessor.	K ₂ , K ₄
CO 3	Illustrate how the different peripherals (8085/8086) are interfaced with Microprocessor.	K ₃
CO 4	Analyze the properties of Microprocessors (8085/8086)	K ₄
CO 5	Evaluate the data transfer information through serial & parallel ports.	K ₅
DETAILED SYLLABUS		3-1-0
Unit	Topic	Proposed Lecture
I	Microprocessor evolution and types, microprocessor architecture and operation of its components, addressing modes, interrupts, data transfer schemes, instruction and data flow, timer and timing diagram, Interfacing devices.	08
II	Pin diagram and internal architecture of 8085 microprocessor, registers, ALU, Control & status, interrupt and machine cycle. Instruction sets. Addressing modes. Instruction formats Instruction Classification: data transfer, arithmetic operations, logical operations, branching operations, machine control and assembler directives.	08
III	Architecture of 8086 microprocessor: register organization, bus interface unit, execution unit, memory addressing, and memory segmentation. Operating modes. Instruction sets, instruction format, Types of instructions. Interrupts: hardware and software interrupts.	08
IV	Assembly language programming based on intel 8085/8086. Instructions, data transfer, arithmetic, logic, branch operations, looping, counting, indexing, programming techniques, counters and time delays, stacks and subroutines, conditional call and return instructions	08
V	Peripheral Devices: 8237 DMA Controller, 8255 programmable peripheral interface, 8253/8254 programmable timer/counter, 8259 programmable interrupt controller, 8251 USART and RS232C.	08
Text books:		
<ol style="list-style-type: none"> 1 Gaonkar, Ramesh S, "Microprocessor Architecture, Programming and Applications with 2 8085", Penram International Publishing. 3 Ray A K, Bhurchandi K M, "Advanced Microprocessors and Peripherals", TMH 4 Hall D V, "Microprocessor Interfacing", TMH 5 Liu and, "Introduction to Microprocessor", TMH 6 Brey, Barry B, "INTEL Microprocessors", PHI 7 Renu Sigh & B.P. Gibson G A, "Microcomputer System: The 8086/8088 family", PHI 8 Aditya P Mathur Sigh, "Microprocessor, Interfacing and Applications M Rafiqzaman, "Microprocessors, Theory and Applications 9 J.L. Antonakos, An Introduction to the Intel Family of Microprocessors, Pearson, 1999 		

Operating Systems Lab (KCS451)

1. Study of hardware and software requirements of different operating systems (UNIX,LINUX,WINDOWS XP, WINDOWS7/8)
2. Execute various UNIX system calls for
 - i. Process management
 - ii. File management
 - iii. Input/output Systems calls
3. Implement CPU SchedulingPolicies:
 - i. SJF
 - ii. Priority
 - iii. FCFS
 - iv. Multi-level Queue
4. Implement file storage allocation technique:
 - i. Contiguous(using array)
 - ii. Linked –list(using linked-list)
 - iii. Indirect allocation(indexing)
5. Implementation of contiguous allocation techniques:
 - i. Worst-Fit
 - ii. Best-Fit
 - iii. First- Fit
6. Calculation of external and internal fragmentation
 - i. Free space list of blocks from system
 - ii. List process file from the system
7. Implementationofcompactionforthecontinuallychangingmemorylayoutandcalculatetotalmovementofdata
8. Implementation of resource allocation graph (RAG)
9. Implementation of Banker’salgorithm
10. Conversion of resource allocation graph (RAG) to wait for graph (WFG) for each type of method used for storing graph.
11. ImplementthesolutionforBoundedBuffer(producer-consumer)problemusinginterprocesscommunication techniques- Semaphores
12. ImplementthesolutionsforReaders-Writersproblemusinginterprocesscommunicationtechnique-Semaphore

Microprocessor Lab (KCS452)

1. Write a program using 8085 Microprocessor for Decimal, Hexadecimal addition and subtraction of two Numbers.
2. Write a program using 8085 Microprocessor for addition and subtraction of two BCD numbers.
3. To perform multiplication and division of two 8 bit numbers using 8085.
4. To find the largest and smallest number in an array of data using 8085 instruction set.
5. To write a program to arrange an array of data in ascending and descending order.
6. To convert given Hexadecimal number into its equivalent ASCII number and vice versa using 8085 instruction set.
7. To write a program to initiate 8251 and to check the transmission and reception of character.
8. To interface 8253 programmable interval timer to 8085 and verify the operation of 8253 in six different modes.
9. To interface DAC with 8085 to demonstrate the generation of square, sawtooth and triangular wave.
10. Serial communication between two 8085 through RS-232 C port.

Python Language Programming Lab (KCS453)

1. To write a python program that takes in command line arguments as input and print the number of arguments.
2. To write a python program to perform Matrix Multiplication.
3. To write a python program to compute the GCD of two numbers.
4. To write a python program to find the most frequent words in a text file.
5. To write a python program find the square root of a number (Newton's method).
6. To write a python program exponentiation (power of a number).
7. To write a python program find the maximum of a list of numbers.
8. To write a python program linear search.
9. To write a python program Binary search.
10. To write a python program selection sort.
11. To write a python program Insertion sort.
12. To write a python program merge sort.
13. To write a python program first n prime numbers.
14. To write a python program simulate bouncing ball in Pygame.

Bundelkhand Institute of Engineering & Technology,
Jhansi



Syllabus
of
B.Tech
Computer Science & Engineering
3rd Year (V & VI Semester)

STUDY & EVALUATION SCHEME
B. Tech computer science & Engineering

YEAR III, SEMESTER V

S. No.	Subject Code	Subject	Period	Evaluation Scheme			Exam	Total
				Sessional				
				CT	TA	Total		
1.	EHU-501	Engineering & Managerial Economic	3-1-0	30	20	50	100	150
2.	ECS-501	Operating System	3-1-0	30	20	50	100	150
3.	ECS-502	Design & Analysis of Algorithms	3-1-0	30	20	50	100	150
4.	ECS-503	Object Oriented Techniques	3-1-0	30	20	50	100	150
5.	ECS-504	Computer Graphics	2-1-0	15	10	25	50	75
6.	ECS-505	Graph Theory	2-1-0	15	10	25	50	75
Practical /Training /Project								
7.	ECS-551	Operating System lab	0-0-2	-	25	25	25	50
8.	ECS-552	Algorithms Lab	0-0-2	-	25	25	25	50
9.	ECS-553	Object Oriented Technique Lab	0-0-2	-	25	25	25	50
10.	ECS-554	Computer Graphics Lab	0-0-2	-	25	25	25	50
11.	GP-501	General proficiency	-	-	-	-	-	50

*At least 10 problems are to be considered based on corresponding theory course.

YEAR III, SEMESTER VI

S. No.	Subject Code	Subject	Period	Evaluation Scheme				Total
				Sessional			Exam	
				CT	TA	Total		
1.	EHU-601	Industrial Management	3-1-0	30	20	50	100	150
2.	ECS-601	Computer Network	3-1-0	30	20	50	100	150
3.	ECS-602	Software Engineering	3-1-0	30	20	50	100	150
4.	ECS-603	Compiler Design	3-1-0	30	20	50	100	150
5.	ECS-604	Web Technology	2-1-0	15	10	25	50	75
6.	ECS-605	Information Security and Cyber Laws	2-1-0	15	10	25	50	75
Practical /Training /Project								
7.	ECS-651	Computer Network lab	0-0-2	-	25	25	25	50
8.	ECS-652	Web Technology based software engineering lab	0-0-2	-	25	25	25	50
9.	ECS-653	Compiler Design lab	0-0-2	-	25	25	25	50
10.	ECS-654	Seminar	0-0-2	-	50	50	-	50
11.	GP-601	General proficiency	-	-	-	-	-	50

*At least 10 problems are to be considered based on corresponding theory course.

ECS-501: Operating System

L	T	P
3	1	0

Unit I

Introduction : Operating system and functions, Classification of Operating systems- Batch, Interactive, Time sharing, Real Time System, Multiprocessor Systems, Multiuser Systems, Multiprocessor Systems, Multithreaded Systems, Operating System Structure- Layered structure, System Components, Operating System services, Reentrant Kernels, Monolithic and Microkernel Systems.

Unit II

Concurrent Processes: Process Concept, Principle of Concurrency, Producer / Consumer Problem, Mutual Exclusion, Critical Section Problem, Dekker's solution, Peterson's solution, Semaphores, Test and Set operation; Classical Problem in Concurrency- Dining Philosopher Problem, Sleeping Barber Problem; Inter Process Communication models and Schemes, Process generation.

Unit III

CPU Scheduling: Scheduling Concepts, Performance Criteria, Process States, Process Transition Diagram, Schedulers, Process Control Block (PCB), Process address space, Process identification information, Threads and their management, Scheduling Algorithms, Multiprocessor Scheduling. Deadlock: System model, Deadlock characterization, Prevention, Avoidance and detection, Recovery from deadlock.

Unit IV

Memory Management: Basic bare machine, Resident monitor, Multiprogramming with fixed partitions, Multiprogramming with variable partitions, Protection schemes, Paging, Segmentation, Paged segmentation, Virtual memory concepts, Demand paging, Performance of demand paging, Page replacement algorithms, Thrashing, Cache memory organization, Locality of reference.

Unit V

I/O Management and Disk Scheduling: I/O devices, and I/O subsystems, I/O buffering, Disk storage and disk scheduling, RAID. File System: File concept, File organization and access mechanism, File directories, and File sharing, File system implementation issues, File system protection and security.

References:

1. Silberschatz, Galvin and Gagne, "Operating Systems Concepts", Wiley
2. Sibsankar Halder and Alex A Aravind, "Operating Systems", Pearson Education
3. Harvey M Dietel, " An Introduction to Operating System", Pearson Education
4. D M Dhamdhare, "Operating Systems : A Concept based Approach", 2nd Edition, TMH
5. William Stallings, "Operating Systems: Internals and Design Principles ", 6th Edition, Pearson Education

ECS-502: Design and Analysis of Algorithms

L	T	P
3	1	0

Unit I

Introduction: Algorithms, Analyzing algorithms, Complexity of algorithms, Growth of functions, Performance measurements, Solving the recursive equations using Substitution Method, Recursive Tree Method and Masters Theorem(with Proof), Searching, Sorting and Order Statistics – Linear Search, Binary Search, Bubble Sort, Insertion Sort, Selection Sort, Quick Sort, Merge Sort, Heap Sort, Comparison of sorting algorithms, Sorting in linear time – Counting Sort, Bucket Sort.

Unit II

Introduction to Recursion: Tail Recursion, Non-tail Recursion, and its example.

Divide and Conquer: Binary Search, Merge Sort, Quick Sort, Max-Min Problem, Powering a Number, Fibonacci Series, and Tower of Hanoi.

Unit III

Greedy method: Optimal Resource Allocation, Job Scheduling, Fractional Knapsack Problem, Huffman's Algorithm, Minimum Spanning trees – Prim's and Kruskal's algorithms, Single source shortest paths – Dijkstra and Bellman-Ford algorithm.

Unit IV

Dynamic programming: Longest Common Subsequence Problem, 0/1-Knapsack, Matrix Chain Multiplication Problem, All pair shortest paths – Floyd Warshal algorithm.

Backtracking, Branch and Bound: Travelling Salesman Problem, Graph Coloring, n-Queen Problem, Sum of subsets, Hamiltonian Cycles.

Unit V

Graph Algorithms: Breath First Search, Depth First Search.

Selected Topics: String Matching, Theory of NP-completeness, Approximation algorithms and Randomized Algorithms.

References:

1. Thomas H. Cormen, Charles E. Leiserson and Ronald L. Rivest, "Introduction to Algorithms", Printice Hall of India.
2. RCT Lee, SS Tseng, RC Chang and YT Tsai, "Introduction to the Design and Analysis of Algorithms", Mc Graw Hill, 2005.
3. E. Horowitz & S Sahni, "Fundamentals of Computer Algorithms",
4. Berman, Paul, "Algorithms", Cengage Learning.
5. Aho, Hopcraft, Ullman, "The Design and Analysis of Computer Algorithms" Pearson Education, 2008.

ECS-503: Object Oriented Techniques

L	T	P
3	1	0

Unit I

Introduction: The meaning of Object Orientation, object identity, Encapsulation, information hiding, polymorphism, generosity, importance of modeling, principles of modeling, object Oriented modeling, Introduction to UML, conceptual model of the UML, Architecture.

Unit II

Basic Structural Modeling: Classes, Relationships, common Mechanisms, and diagrams. Class & Object Diagrams: Terms, concepts, modeling techniques for Class & Object Diagrams. Collaboration Diagrams: Terms, Concepts, depicting a message, polymorphism in collaboration Diagrams, iterated messages, use of self in messages. Sequence Diagrams: Terms, concepts, Depicting asynchronous messages with/without priority, callback mechanism, broadcast messages. Basic Behavioral Modeling: Use cases, Use case Diagrams, Activity Diagrams, State Machine ,Process and thread, Event and signals, Time diagram, interaction diagram, Package diagram

Unit III

Object Oriented Analysis, Object oriented design, Object design, combining three models, Designing algorithms, design optimization, Implementation of control, Adjustment of inheritance, Object representation. Structured analysis and structured design (SA/SD), Jackson Structured Development (JSD). Mapping object oriented concepts using non-object oriented language, Translating classes into data structures, Passing arguments to methods, Implementing inheritance, associations encapsulation. Object oriented programming style: reusability, extensibility, robustness, programming in the large. Procedural v/s OOP, Object oriented language features. Abstraction and Encapsulation.

Unit IV

Introduction to Java, History, Features, Object Oriented concept of Java, Classes and Objects, Inheritance, Packages, Interface , abstract method and classes, Polymorphism, Inner classes, String Handling, I/O , Networking, Event Handling. Multi-threading, Collection, Java APIs, Java Beans: Application Builder tools, The bean developer kit(BDK), JAR files, Introspection, Developing a simple bean, using Bound properties, The Java Beans API, Session Beans, Entity Beans, Introduction to Enterprise Java beans (EJB).

Unit V

Java Swing: Introduction to AWT, AWT v/s Swing, Creating a Swing Applet and Application. Utility of Java as internet programming language, JDBC, The connectivity model, JDBC/ODBC Bridge, Introduction to servlets.

References:

1. James Rumbaugh et. al, "Object Oriented Modeling and Design", PHI
2. Grady Booch, James Rumbaugh, Ivar Jacobson, "The Unified Modeling Language User Guide", Pearson Education
3. Naughton, Schildt, "The Complete Reference JAVA2", TMH
4. Mark Priestley "Practical Object-Oriented Design with UML", TMH
5. Booch, Maksimchuk, Engle, Young, Conallen and Houston, "Object Oriented Analysis and Design with Applications", Pearson Education
6. Pandey, Tiwari, " Object Oriented Programming with JAVA" , Acme Learning

ECS-504: Computer Graphics

L	T	P
2	1	0

Unit I

Introduction and Line Generation: Types of computer graphics, Graphic Displays- Random scan displays, Raster scan displays, Frame buffer and video controller, Points and lines, Line drawing algorithms, Circle generating algorithms, Midpoint circle generating algorithm, and parallel version of these algorithms.

Unit II

Transformations: Basic transformation, Matrix representations and homogenous coordinates, Composite transformations, Reflections and shearing. Windowing and Clipping: Viewing pipeline, Viewing transformations, 2-D Clipping algorithms-Line clipping algorithms such as Cohen Sutherland line clipping algorithm, Liang Barsky algorithm, Line clipping against non-rectangular clip windows; Polygon clipping – Sutherland Hodgeman polygon clipping, Weiler and Atherton polygon clipping, Curve clipping, Text clipping.

Unit III

Three Dimensional: 3-D geometric primitives, 3-D Object representation, 3-D Transformation, 3-D viewing, projections, 3-D Clipping.

Unit IV

Curves and Surfaces: Quadric surfaces, Spheres, Ellipsoid, Blobby objects, Introductory concepts of Spline, Bspline and Bezier curves and surfaces. Hidden Lines and Surfaces: Back Face Detection algorithm, Depth buffer method, A- buffer method, Scan line method, basic illumination models – Ambient light, Diffuse reflection, Specular reflection and Phong model, Combined approach, Warn model, Intensity Attenuation, Color consideration, Transparency and Shadows.

References:

1. Donald Hearn and M Pauline Baker, “Computer Graphics C Version”, Pearson Education
2. Amrendra N Sinha and Arun D Udai,” Computer Graphics”, TMH
3. Donald Hearn and M Pauline Baker, “Computer Graphics with OpenGL”, Pearson education
4. Steven Harrington, “Computer Graphics: A Programming Approach” , TMH
5. Rogers, “ Procedural Elements of Computer Graphics”, McGraw Hill

ECS-505: Graph Theory

L	T	P
2	1	0

Unit I

Graphs, Sub graphs, some basic properties, various example of graphs & their sub graphs, walks, path & circuits, connected graphs, disconnected graphs and component, euler graphs, various operation on graphs, Hamiltonian paths and circuits, the traveling sales man problem.

Unit II

Trees and fundamental circuits, distance diameters, radius and pendent vertices, rooted and binary trees, on counting trees, spanning trees, fundamental circuits, finding all spanning trees of a graph and a weighted graph, algorithms of primes, Kruskal and Dijkstra Algorithms.

Unit III

Cuts sets and cut vertices, some properties, all cut sets in a graph, fundamental circuits and cut sets , connectivity and separability, network flows Planer graphs, combinatorial and geometric dual: Kuratowski graphs, detection of planarity, geometric dual, Discussion on criterion of planarity, thickness and crossings.

Unit IV

Vector space of a graph and vectors, basis vector, cut set vector, circuit vector, circuit and cut set subspaces, Matrix representation of graph – Basic concepts; Incidence matrix, Circuit matrix, Path matrix, Cut-set matrix and Adjacency matrix. Coloring, covering and partitioning of a graph, chromatic number, chromatic partitioning, chromatic polynomials, matching, covering, four color problem Discussion of Graph theoretic algorithm wherever required.

References

1. Deo, N, Graph theory with applications to Engineering and Computer Science, PHI
2. Gary Chartrand and Ping Zhang, Introduction to Graph Theory, TMH
3. Robin J. Wilson, Introduction to Graph Theory, Pearson Education
4. Harary, F, Graph Theory, Narosa

ECS-551: Operating System Lab

L	T	P
0	0	2

1. RR(Round Robin) Scheduling
2. SJF(Shortest Job First)
3. FCFS(First Come First Served)
4. Priority Scheduling
5. Sequential File Allocation
6. Indexed File Allocation
7. Linked File Allocation
8. Simulate MVT and MFT
9. MVT(Multiprogramming Variable Task)
10. MFT(Multiprogramming Fixed Task)
11. Banker's Algorithm for Dead Lock Avoidance and Dead Lock Prevention
12. FIFO(First In First Out) Page Replacement
13. LRU(Least Recent Used) Page Replacement
14. Optimal Page Replacement (LFU)
15. Paging Memory Allocation Technique
16. Segmentation Memory Allocation Technique
17. Lexical Analyzer

ECS-552 Algorithms Lab

L	T	P
0	0	2

Programming assignments on each algorithmic strategy:

1. Divide and conquer method (quick sort, merge sort, Strassen's matrix multiplication),
2. Greedy method (knapsack problem, job sequencing, optimal merge patterns, minimal spanning trees).
3. Dynamic programming (multistage graphs, OBST, 0/1 knapsack, traveling salesperson problem).
4. Back tracking (n-queens problem, graph coloring problem, Hamiltonian cycles).
5. Sorting: Insertion sort, Heap sort, Bubble sort
6. Searching: Sequential and Binary Search
7. Selection: Minimum/ Maximum, Kth smallest element

ECS-553 Object Oriented Technique Lab

L	T	P
0	0	2

1. Write a program in java which prints your name using command line arguments.
2. Write a program in java which enters three number using command line arguments and print sum and average of the number
3. Write a program to swap the value of 2 variables without using 3rd variable
4. Write a program to calculate the sum of digits of a given integer no.
5. Write a program to compute the sum of the first and last digit of a given number.
6. Write a program to calculate and print first n Fibonacci numbers.
7. Write a program to reverse the given number.
8. Write a program in java which enter the number using Data Input Stream and check whether the entered number is even or odd.
9. Write a program that calculate and print the roots of a quadratic equation $ax^2+bx+c = 0$ and appropriate message should be printed if root are complex
10. Write an application that reads a string and determines whether it is a palindrome.
11. Write a program to enter a sentence form keyboard and also find all the words in that sentence with starting character as vowel.
12. Write a Program in java which creates the array of size 5; find the sum and average of the five numbers.
13. Create a java program that has three version of add method which can add two, three, and four integers
14. Write a Java program that uses an overloaded method volume() that returns volume of different structures.

ECS-554 Computer Graphics Lab

L	T	P
0	0	2

Write a program for 2D line drawing using DDA algorithm.

1. Write a program to draw a line using Bresenham's Algo.
2. Write a program for circle drawing as Raster Graphics Display.
3. Write a program to draw a circle using Midpoint algo. Modify the same for drawing an arc and sector.
4. Write a program to rotate a point about origin.
5. Write a program to rotate a triangle about origin.
6. Write a program to scale the triangle.
7. Write a program to translate the triangle.
8. Write a program to reflect the triangle.
9. Write a program for polygon filling as Raster Graphics Display.
10. Write a program for line clipping.
11. Write a program for polygon clipping.
12. Write a program for displaying 3D objects as 2D display using perspective transformation.
13. Write a program for rotation of a 3D object about arbitrary axis.
14. Write a program for Hidden surface removal from a 3D object.

ECS-601: Computer Network

L	T	P
3	1	0

Unit I

Introduction Concepts: Goals and Applications of Networks, Network structure and architecture, The OSI reference model, services, Network Topology Design - Delay Analysis, Back Bone Design, Local Access Network Design, Physical Layer Transmission Media, Switching methods, ISDN, Terminal Handling.

Unit II

Medium Access sub layer: Medium Access sub layer - Channel Allocations, LAN protocols - ALOHA protocols - Overview of IEEE standards - FDDI. Data Link Layer - Elementary Data Link Protocols, Sliding Window protocols, Error Handling.

Unit III

Network Layer: Network Layer - Point - to Pont Networks, routing, Congestion control Internetworking -TCP / IP, IP packet, IP address, IPv6.

Unit IV

Transport Layer: Transport Layer - Design issues, connection management, session Layer-Design issues, remote procedure call. Presentation Layer-Design issues, Data compression techniques, cryptography - TCP - Window Management.

Unit V

Application Layer: Application Layer: File Transfer, Access and Management, Electronic mail, Virtual Terminals, Other application. Example Networks - Internet and Public Networks.

References:

1. Forouzen, "Data Communication and Networking", TMH
2. A.S. Tanenbaum, Computer Networks, Pearson Education
3. W. Stallings, Data and Computer Communication, Macmillan Press
4. Anuranjan Misra, "Computer Networks", Acme Learning
5. G. Shanmugarathinam, "Essential of TCP/ IP", Firewall Media

ECS-602: Software Engineering

L	T	P
3	1	0

Unit I

Introduction to Software Engineering, Software Components, Software Characteristics, Software Crisis, Software Engineering Processes, Similarity and Differences from Conventional Engineering Processes, Software Quality Attributes. Software Development Life Cycle (SDLC)
Models: Water Fall Model, Prototype Model, Spiral Model, Evolutionary Development Models, Iterative Enhancement Models

Unit II

Software Requirement Specifications (SRS) Requirement Engineering Process: Elicitation, Analysis, Documentation, Review and Management of User Needs, Feasibility Study, Information Modeling, Data Flow Diagrams, Entity Relationship Diagrams, Decision Tables, SRS Document, IEEE Standards for SRS.

Software Quality Assurance (SQA): Verification and Validation, SQA Plans, Software Quality Frameworks, ISO 9000 Models, SEI-CMM Model.

Unit III

Software Design Basic Concept of Software Design, Architectural Design, Low Level Design: Modularization, Design Structure Charts, Pseudo Codes, Flow Charts, Coupling and Cohesion Measures, Design Strategies: Function Oriented Design, Object Oriented Design, Top-Down and Bottom-Up Design. Software Measurement and Metrics: Various Size Oriented Measures: Halstead's Software Science, Function Point (FP) Based Measures, Cyclomatic Complexity Measures: Control Flow Graphs.

Unit IV

Software Testing: Testing Objectives, Unit Testing, Integration Testing, Acceptance Testing, Regression Testing, Testing for Functionality and Testing for Performance, Top-Down and Bottom-Up Testing Strategies: Test Drivers and Test Stubs, Structural Testing (White Box Testing), Functional Testing (Black Box Testing), Test Data Suit Preparation, Alpha and Beta Testing of Products. Static Testing Strategies: Formal Technical Reviews (Peer Reviews), Walk Through, Code Inspection, Compliance with Design and Coding Standards.

Unit V

Software Maintenance and Software Project Management Software as an Evolutionary Entity, Need for Maintenance, Categories of Maintenance: Preventive, Corrective and Perfective Maintenance, Cost of Maintenance, Software Re-Engineering, Reverse Engineering. Software Configuration Management Activities, Change Control Process, Software Version Control, An Overview of CASE Tools. Estimation of Various Parameters such as Cost, Efforts, Schedule/Duration, Constructive Cost Models (COCOMO), Resource Allocation Models, Software Risk Analysis and Management.

References:

1. R. S. Pressman, Software Engineering: A Practitioners Approach, McGraw Hill.
2. Rajib Mall, Fundamentals of Software Engineering, PHI Publication.
3. K. K. Aggarwal and Yogesh Singh, Software Engineering, New Age International Publishers.

4. Pankaj Jalote, Software Engineering, Wiley
5. Carlo Ghezzi, M. Jarayeri, D. Manodrioli, Fundamentals of Software Engineering, PHI Publication.
6. Ian Sommerville, Software Engineering, Addison Wesley.
7. Kassem Saleh, "Software Engineering", Cengage Learning.
8. Pfleeger, Software Engineering, Macmillan Publication.

ECS-603: Compiler Design

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Unit I

Introduction to Compiler, Phases and passes, Bootstrapping, Finite state machines and regular expressions and their applications to lexical analysis, Optimization of DFA-Based Pattern Matchers implementation of lexical analyzers, lexical-analyzer generator, LEX-compiler, Formal grammars and their application to syntax analysis, BNF notation, ambiguity, YACC. The syntactic specification of programming languages: Context free grammars, derivation and parse trees, capabilities of CFG.

Unit II

Basic Parsing Techniques: Parsers, Shift reduce parsing, operator precedence parsing, top down parsing, predictive parsers Automatic Construction of efficient Parsers: LR parsers, the canonical Collection of LR(0) items, constructing SLR parsing tables, constructing Canonical LR parsing tables, Constructing LALR parsing tables, using ambiguous grammars, an automatic parser generator, implementation of LR parsing tables.

Unit III

Syntax-directed Translation: Syntax-directed Translation schemes, Implementation of Syntax directed Translators, Intermediate code, postfix notation, Parse trees & syntax trees, three address code, quadruple & triples, translation of assignment statements, Boolean expressions, statements that alter the flow of control, postfix translation, translation with a top down parser. More about translation: Array references in arithmetic expressions, procedures call, declarations and case statements.

Unit IV

Symbol Tables: Data structure for symbols tables, representing scope information. Run-Time Administration: Implementation of simple stack allocation scheme, storage allocation in block structured language. Error Detection & Recovery: Lexical Phase errors, syntactic phase errors semantic errors.

Unit V

Code Generation: Design Issues, the Target Language. Addresses in the Target Code, Basic Blocks and Flow Graphs, Optimization of Basic Blocks, Code Generator. Code optimization: Machine-Independent Optimizations, Loop optimization, DAG representation of basic blocks, value numbers and algebraic laws, Global Data-Flow analysis.

References:

1. Aho, Sethi & Ullman, "Compilers: Principles, Techniques and Tools", Pearson Education.
2. V Raghvan, "Principles of Compiler Design", TMH.
3. Kenneth Loudon, "Compiler Construction", Cengage Learning.
4. Charles Fischer and Ricard LeBlanc, "Crafting a Compiler with C", Pearson Education.

ECS-604 Web Technology

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Unit I

Introduction

Introduction to web, protocols governing the web, web development strategies, web applications, web project, web team.

Unit II

Web Page Designing

HTML: list, table, images, frames, forms, CSS;XML: DTD, XML schemes, presenting and using XML

Unit III

Scripting

Java script: Introduction, documents, forms, statements, functions, objects; Event and event handling; introduction to AJAX, VB Script, CGI

Unit IV

Server Site Programming

Introduction to active server pages (ASP), ASP.NET, java server pages (JSP), JSP application design, tomcat server, JSP objects, declaring variables and methods, debugging, sharing data between JSP pages, Session, introduction to COM/DCOM.

References

1. Xavier, C, " Web Technology and Design" , New Age International
2. Deitel, "Java for programmers", Pearson Education
3. Ivan Bayross," HTML, DHTML, Java Script, Perl & CGI", BPB Publication.
4. Ramesh Bangia, "Internet and Web Design" , New Age International
5. Jackson, "Web Technologies" Pearson Education
6. Patel and Barik, "Introduction to Web Technology & Internet", Acme Learning

ECS-605 Information Security and Cyber Laws

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2	1	0

Unit I

History of Information Systems and its Importance, basics, Changing Nature of Information Systems, Need of Distributed Information Systems, Role of Internet and Web Services, Information System Threats and attacks, Classification of Threats and Assessing Damages 18 Security in Mobile and Wireless Computing- Security Challenges in Mobile Devices, authentication Service Security, Security Implication for organizations, Laptops Security Basic Principles of Information Security, Confidentiality, Integrity Availability and other terms in Information Security, Information Classification and their Roles.

Unit II

Security Threats to E Commerce, Virtual Organization, Business Transactions on Web, E Governance and EDI, Concepts in Electronics payment systems, E Cash, Credit/Debit Cards. Physical Security- Needs, Disaster and Controls, Basic Tenets of Physical Security and Physical Entry Controls, Access Control- Biometrics, Factors in Biometrics Systems, Benefits, Criteria for selection of biometrics, Design Issues in Biometric Systems, Interoperability Issues, Economic and Social Aspects, Legal Challenges

Unit III

Model of Cryptographic Systems, Issues in Documents Security, System of Keys, Public Key Cryptography, Digital Signature, Requirement of Digital Signature System, Finger Prints, Firewalls, Design and Implementation Issues, Policies Network Security- Basic Concepts, Dimensions, Perimeter for Network Protection, Network Attacks, Need of Intrusion Monitoring and Detection, Intrusion Detection Virtual Private Networks- Need, Use of Tunneling with VPN, Authentication Mechanisms, Types of VPNs and their Usage, Security Concerns in VPN

Unit IV

Security metrics- Classification and their benefits Information Security & Law, IPR, Patent Law, Copyright Law, Legal Issues in Data mining Security, Building Security into Software Life Cycle Ethics- Ethical Issues, Issues in Data and Software Privacy Cyber Crime Types & overview of Cyber Crimes.

References :

1. Godbole, "Information Systems Security", Willey
2. Merkov, Breithaupt, "Information Security", Pearson Education
3. Yadav, "Foundations of Information Technology", New Age, Delhi
4. Schou, Shoemaker, "Information Assurance for the Enterprise", Tata McGraw Hill
5. Sood, "Cyber Laws Simplified", Mc Graw Hill
6. Furnell, "Computer Insecurity", Springer

ECS-651 Computer Network Lab

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1. Write a program for error detecting code using CRC-CCITT.
2. Write a program for frame sorting technique used in buffers.
3. Write a program for distance vector algorithm to find suitable path for transmission.
4. Write a program for spanning tree algorithm (Kruskal's/Prim's) to find loop less path.
5. Write a program for simple RSA algorithm to encrypt and decrypt the data.
6. Write a program for Hamming Code generation for error detection and correction.
7. Write a program for congestion control using Leaky bucket algorithm.

ECS-652 Web Technology Based Software engineering Lab

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1. Write HTML/Java scripts to display your CV in Web Browser.
2. Creation and annotation of static web pages using any HTML editor.
3. Write a program to use XML and JavaScript for creation of your homepage.
4. Write a program in XML for creation of DTD which specifies a particular set of rules.
5. Create a Stylesheet in CSS/XSL and display the document in Web Browser.
6. Write a Java Servlet for HTTP Proxy Server.
7. Use JSP pages for sharing session and application data of HTTP Server.
8. Write a program to use JDBC connectivity program for maintaining database by sending queries

ECS-653 Compiler Lab

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1. To check whether a string belongs to a grammar or not.
2. To calculate leading for all the non -terminals of the given grammar.
3. Program for computation of first algorithm.
4. Program to find the number of white spaces and new lines characters.
5. To implement stack using array.
6. Algorithm to identify whether a given string is an identifier or not.
7. Algorithm to check whether a string is a keyword or not
8. Design a lexical analyzer for given language .the lexical analyzer should ignore redundant spaces, tabs and new lines
9. Implement the lexical analyzer using JLex, flex or other lexical analyzer generating tools.
10. Design predictive parser for the given language
11. Design a LALR bottom up parser for the given language
12. Convert the BNF rules into Yacc form and write code to generate abstract syntax tree.
13. A program to generate machine code

Bundelkhand Institute of Engineering & Technology,
Jhansi



Syllabus
of
B.Tech
Computer Science & Engineering
4th Year (VII & VIII Semester)

STUDY & EVALUATION SCHEME
B. Tech computer science & Engineering
YEAR IV, SEMESTER VII

S. No.	Subject Code	Subject	Period	Evaluation Scheme				Total
				Sessional			Exam	
				CT	TA	Total		
1.	EOE-071 / EOE-074	Open Elective- I	3-1-0	30	20	50	100	150
2.	ECS-701	Distributed Systems	3-1-0	30	20	50	100	150
3.	ECS-702	Digital Image Processing	3-1-0	30	20	50	100	150
4.	ECS-074	Pattern Recognition	3-1-0	30	20	50	100	150
5.	ECS-075	Data Mining & Data Warehousing	3-1-0	30	20	50	100	150
Practical /Training /Project								
6.	ECS-751	Distributed System lab	0-0-2	-	25	25	25	50
7.	ECS-752	Digital Image Processing lab	0-0-2	-	25	25	25	50
8.	ECS-753	Project	0-0-4	-	50	50	-	50
9.	ECS-754	Industrial Training Viva-voice	0-0-2	-	50	50	-	50
10.	GP-701	General proficiency	-	-	-	-	-	50

YEAR IV, SEMESTER VIII

S. No.	Subject Code	Subject	Period	Evaluation Scheme				Total
				Sessional			Exam	
				CT	TA	Total		
1.	EOE-081/ EOE-084	Open Elective II	3-1-0	30	20	50	100	150
2.	ECS-801	Artificial Intelligence	3-1-0	30	20	50	100	150
3.	ECS-081	Real time system	3-1-0	30	20	50	100	150
4.	ECS-087	Mobile computing	3-1-0	30	20	50	100	150
Practical /Training /Project								
5.	ECS-851	Artificial Intelligence lab	0-0-2	-	25	25	25	50
6.	ECS-852	Project	0-0-12	-	100	100	200	300
7.	GP-801	General proficiency	-	-	-	-	-	50

Note:

Practical Training done after 6th semester would be evaluated in 7th semester through report and viva-voce. Practical has to be initiated in 7th semester beginning and completed by the end of 8th semester with proper report and demonstration.

*At least 10 problems are to be considered based on corresponding theory course.

List of Effective for B.Tech

(Computer Science & Technology)

CS Elective-I

- ECS-071 Computational Geometry
- ECS-072 Computational Complexity
- ECS-073 Parallel Algorithm
- ECS-074 Pattern Recognition

CS Elective-II

- ECS-075 Data Mining & Data Warehousing
- ECS-076 Distributed Database
- EIT-073 Bioinformatics
- ECS-077 Data Compression
- EIT-074 IT in Forensic Science

CS Elective-III

- ECS-081 Real Time System
- ECS-082 Software Project Management
- ECS-083 Embedded Systems
- ECS-084 Cryptography & Network Security

CS Elective-IV

- ECS-085 Neural Network
- ECS-086 Natural Language Processing
- ECS-087 Mobile Computing
- ECS-088 Soft Computing

Open Elective-I

HCI - Human Computing Interaction

Note: ECS-088 may be opted by only those students who did not opt EOE-041 as an open elective

ECS-701 Distributed Systems

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Unit I

Characterization of Distributed Systems: Introduction, Examples of distributed Systems, Resource sharing and the Web Challenges. Architectural models, Fundamental Models. Theoretical Foundation for Distributed System: Limitation of Distributed system, absence of global clock, shared memory, Logical clocks, Lamport's & vectors logical clocks. Concepts in Message Passing Systems: causal order, total order, total causal order, Techniques for Message Ordering, Causal ordering of messages, global state, termination detection.

Unit II

Distributed Mutual Exclusion: Classification of distributed mutual exclusion, requirement of mutual exclusion theorem, Token based and non token based algorithms, performance metric for distributed mutual exclusion algorithms.

Distributed Deadlock Detection: system model, resource Vs communication deadlocks, deadlock prevention, avoidance, detection & resolution, centralized dead lock detection, distributed dead lock detection, path pushing algorithms, edge chasing algorithms.

Unit III

Agreement Protocols: Introduction, System models, classification of Agreement Problem, Byzantine agreement problem, Consensus problem, Interactive consistency Problem, Solution to Byzantine Agreement problem, Application of Agreement problem, Atomic Commit in Distributed Database system.

Distributed Resource Management: Issues in distributed File Systems, Mechanism for building distributed file systems, Design issues in Distributed Shared Memory, Algorithm for Implementation of Distributed Shared Memory.

Unit IV

Failure Recovery in Distributed Systems: Concepts in Backward and Forward recovery, Recovery in Concurrent systems, obtaining consistent Checkpoints, Recovery in Distributed Database Systems.

Fault Tolerance: Issues in Fault Tolerance, Commit Protocols, Voting protocols, Dynamic voting protocols.

Unit V

Transactions and Concurrency Control: Transactions, Nested transactions, Locks, Optimistic Concurrency control, Timestamp ordering, Comparison of methods for concurrency control.

Distributed Transactions: Flat and nested distributed transactions, Atomic Commit protocols, Concurrency control in distributed transactions, Distributed deadlocks, Transaction recovery.

Replication: System model and group communication, Fault - tolerant services, highly available services, Transactions with replicated data.

References:

1. Singhal & Shivaratri, "Advanced Concept in Operating Systems", McGraw Hill
2. Ramakrishna,Gehrke," Database Management Systems", Mc Grawhill
3. Coulouris, Dollimore, Kindberg, "Distributed System: Concepts and Design", Pearson Education
4. Tenanuanbaum, Steen," Distributed Systems", PHI
5. Gerald Tel, "Distributed Algorithms", Cambridge University Press.

ECS-702 Digital Image Processing

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Unit I

Introduction and Fundamentals

Motivation and Perspective, Applications, Components of Image Processing System, Element of Visual Perception, A Simple Image Model, Sampling and Quantization.

Image Enhancement in Frequency Domain

Fourier Transform and the Frequency Domain, Basis of Filtering in Frequency Domain, Filters – Low-pass, High-pass; Correspondence Between Filtering in Spatial and Frequency Domain; Smoothing Frequency Domain Filters – Gaussian Lowpass Filters; Sharpening Frequency Domain Filters – Gaussian Highpass Filters; Homomorphic Filtering.

Unit II

Image Enhancement in Spatial Domain

Introduction; Basic Gray Level Functions – Piecewise-Linear Transformation Functions: Contrast Stretching; Histogram Specification; Histogram Equalization; Local Enhancement; Enhancement using Arithmetic/Logic Operations – Image Subtraction, Image Averaging; Basics of Spatial Filtering; Smoothing - Mean filter, Ordered Statistic Filter; Sharpening – The Laplacian.

Unit III

Image Restoration

A Model of Restoration Process, Noise Models, Restoration in the presence of Noise only-Spatial Filtering – Mean Filters: Arithmetic Mean filter, Geometric Mean Filter, Order Statistic Filters – Median Filter, Max and Min filters; Periodic Noise Reduction by Frequency Domain Filtering – Bandpass Filters; Minimum Mean-square Error Restoration.

Unit IV

Morphological Image Processing

Introduction, Logic Operations involving Binary Images, Dilation and Erosion, Opening and Closing, Morphological Algorithms – Boundary Extraction, Region Filling, Extraction of Connected Components, Convex Hull, Thinning, Thickening

Unit V

Registration

Introduction, Geometric Transformation – Plane to Plane transformation, Mapping, Stereo Imaging – Algorithms to Establish Correspondence, Algorithms to Recover Depth

Segmentation

Introduction, Region Extraction, Pixel-Based Approach, Multi-level Thresholding, Local Thresholding, Region-based Approach, Edge and Line Detection: Edge Detection, Edge Operators, Pattern Fitting Approach, Edge Linking and Edge Following, Edge Elements Extraction by Thresholding, Edge Detector Performance, Line Detection, Corner Detection.

References:

1. Digital Image Processing 2nd Edition, Rafael C. Gonzalvez and Richard E. Woods. Published by: Pearson Education.
2. Digital Image Processing and Computer Vision, R.J. Schalkoff. Published by: John Wiley and Sons, NY.
3. Fundamentals of Digital Image Processing, A.K. Jain. Published by Prentice Hall, Upper Saddle River, NJ.

ECS-074 Pattern Recognition

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Unit I

Introduction: Basics of pattern recognition, Design principles of pattern recognition system, Learning and adaptation, Pattern recognition approaches, Mathematical foundations – Linear algebra, Probability Theory, Expectation, mean and covariance, Normal distribution, multivariate normal densities, Chi squared test.

Unit II

Statistical Pattern Recognition: Bayesian Decision Theory, Classifiers, Normal density and discriminant functions,

Unit III

Parameter estimation methods: Maximum-Likelihood estimation, Bayesian Parameter estimation, Dimension reduction methods - Principal Component Analysis (PCA), Fisher Linear discriminant analysis, Expectation-maximization (EM), Hidden Markov Models (HMM), Gaussian mixture models.

Unit IV

Nonparametric Techniques: Density Estimation, Parzen Windows, K-Nearest Neighbor Estimation, Nearest Neighbor Rule, Fuzzy classification.

Unit V

Unsupervised Learning & Clustering: Criterion functions for clustering, Clustering Techniques: Iterative square - error partition clustering – K means, agglomerative hierarchical clustering, Cluster validation.

References:

1. Richard O. Duda, Peter E. Hart and David G. Stork, "Pattern Classification", 2nd Edition, John Wiley, 2006.
2. C. M. Bishop, "Pattern Recognition and Machine Learning", Springer, 2009.
3. S. Theodoridis and K. Koutroubas, "Pattern Recognition", 4th Edition, Academic Press, 2009.

ECS-075 Data Mining & Data Warehousing

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Unit I

Overview, Motivation(for Data Mining),Data Mining-Definition & Functionalities, Data Processing, Form of Data Preprocessing, Data Cleaning: Missing Values, Noisy Data,(Binning, Clustering, Regression, Computer and Human inspection),Inconsistent Data, Data Integration and Transformation. Data Reduction:-Data Cube Aggregation, Dimensionality reduction, Data 35 Compression, Numerosity Reduction, Clustering, Discretization and Concept hierarchy generation

Unit II

Concept Description:- Definition, Data Generalization, Analytical Characterization, Analysis of attribute relevance, Mining Class comparisons, Statistical measures in large Databases. Measuring Central Tendency, Measuring Dispersion of Data, Graph Displays of Basic Statistical class Description, Mining Association Rules in Large Databases, Association rule mining, mining Single-Dimensional Boolean Association rules from Transactional Databases– Apriori Algorithm, Mining Multilevel Association rules from Transaction Databases and Mining Multi- Dimensional Association rules from Relational Databases

Unit III

Classification and Predictions: What is Classification & Prediction, Issues regarding Classification and prediction, Decision tree, Bayesian Classification, Classification by Back propagation, Multilayer feed-forward Neural Network, Back propagation Algorithm, Classification methods K-nearest neighbor classifiers, Genetic Algorithm. Cluster Analysis:

Data types in cluster analysis, Categories of clustering methods, partitioning methods.

Hierarchical Clustering- CURE and Chameleon, Density Based Methods-DBSCAN, OPTICS, Grid Based Methods- STING, CLIQUE, Model Based Method –Statistical Approach, Neural Network approach, Outlier Analysis

Unit IV

Data Warehousing: Overview, Definition, Delivery Process, Difference between Database System and Data Warehouse, Multi-Dimensional Data Model, Data Cubes, Stars, Snow Flakes, Fact Constellations, Concept hierarchy, Process Architecture, 3 Tier Architecture, Data Marting.

Unit V

Aggregation, Historical information, Query Facility, OLAP function and Tools. OLAP Servers, ROLAP, MOLAP, HOLAP, Data Mining interface, Security, Backup and Recovery, Tuning Data Warehouse, Testing Data Warehouse.

References:

1. M.H.Dunham, "Data Mining:Introductory and Advanced Topics" Pearson Education
2. Jiawei Han, Micheline Kamber, "Data Mining Concepts & Techniques" Elsevier
3. Sam Anahory, Dennis Murray, "Data Warehousing in the Real World: A Practical Guide for Building Decision Support Systems, Pearson Education
4. Mallach,"Data Warehousing System", McGraw –Hill

ECS-751 Distributed Systems Lab

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1. Simulate the functioning of Lamport's Logical Clock in 'C'.
2. Simulate the Distributed Mutual Exclusion in 'C'.
3. Implement a Distributed Chat Server using TCP Sockets in 'C'.
4. Implement RPC mechanism for a file transfer across a network in 'C'
5. Implement 'Java RMI' mechanism for accessing methods of remote systems.
6. Simulate Balanced Sliding Window Protocol in 'C'.
7. Implement CORBA mechanism by using 'C++' program at one end and 'Java' program on the other

ECS-752 Digital Image Processing Lab

L	T	P
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1. Implement the spatial image enhancement functions on a bitmap image –
 - a) Mirroring (Inversion)
 - b) Rotation (Clockwise)
 - c) Enlargement (Double Size)
2. Implement
 - a) Low Pass Filter
 - b) High Pass Filter
3. Implement
 - a) Arithmetic Mean Filter
 - b) Geometric Mean Filter
4. Implement Smoothing and Sharpening of an eight bit color image
5. Implement
 - a) Boundary Extraction Algorithm
 - b) Graham's Scan Algorithm
5. Implement
 - a) Edge Detection
 - b) Line Detection

ECS-801: Artificial Intelligence

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Unit I

Introduction: Introduction to Artificial Intelligence, Foundations and History of Artificial Intelligence, Applications of Artificial Intelligence, Intelligent Agents, Structure of Intelligent Agents. Computer vision, Natural Language Possessing.

Unit II

Introduction to Search: Searching for solutions, Uniformed search strategies, Informed search strategies, Local search algorithms and optimistic problems, Adversarial Search, Search for games, Alpha - Beta pruning.

Unit III

Knowledge Representation & Reasoning: Propositional logic, Theory of first order logic, Inference in First order logic, Forward & Backward chaining, Resolution, Probabilistic reasoning, Utility theory, Hidden Markov Models (HMM), Bayesian Networks.

Unit IV

Machine Learning: Supervised and unsupervised learning, Decision trees, Statistical learning models, Learning with complete data - Naive Bayes models, Learning with hidden data – EM algorithm, Reinforcement learning,

Unit V

Pattern Recognition: Introduction, Design principles of pattern recognition system, Statistical Pattern recognition, Parameter estimation methods - Principle Component Analysis (PCA) and Linear Discriminant Analysis (LDA), Classification Techniques – Nearest Neighbor (NN) Rule, Bayes Classifier, Support Vector Machine (SVM), K – means clustering.

References:

1. Stuart Russell, Peter Norvig, “Artificial Intelligence – A Modern Approach”, Pearson Education
2. Elaine Rich and Kevin Knight, “Artificial Intelligence”, McGraw-Hill
3. E Charniak and D McDermott, “Introduction to Artificial Intelligence”, Pearson Education
4. Dan W. Patterson, “Artificial Intelligence and Expert Systems”, Prentice Hall of India

ECS-081 Real Time System

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Unit I

Introduction

Definition, Typical Real Time Applications: Digital Control, High Level Controls, Signal Processing etc., Release Times, Deadlines, and Timing Constraints, Hard Real Time Systems and Soft Real Time Systems, Reference Models for Real Time Systems: Processors and Resources, Temporal Parameters of Real Time Workload, Periodic Task Model, Precedence Constraints and Data Dependency.

Unit II

Real Time Scheduling

Common Approaches to Real Time Scheduling: Clock Driven Approach, Weighted Round Robin Approach, Priority Driven Approach, Dynamic Versus Static Systems, Optimality of Effective-Deadline-First (EDF) and Least-Slack-Time-First (LST) Algorithms, Rate Monotonic Algorithm, Offline Versus Online Scheduling, Scheduling Aperiodic and Sporadic jobs in Priority Driven and Clock Driven Systems.

Unit III

Resources Sharing

Effect of Resource Contention and Resource Access Control (RAC), Non-preemptive Critical Sections, Basic Priority-Inheritance and Priority-Ceiling Protocols, Stack Based Priority-Ceiling Protocol, Use of Priority-Ceiling Protocol in Dynamic Priority Systems, Preemption Ceiling Protocol, Access Control in Multiple-Unit Resources, Controlling Concurrent Accesses to Data Objects.

Unit IV

Real Time Communication

Basic Concepts in Real time Communication, Soft and Hard RT Communication systems, Model of Real Time Communication, Priority-Based Service and Weighted Round-Robin Service Disciplines for Switched Networks, Medium Access Control Protocols for Broadcast Networks, Internet and Resource Reservation Protocols

Unit V

Real Time Operating Systems and Databases

Features of RTOS, Time Services, UNIX as RTOS, POSIX Issues, Characteristic of Temporal data, Temporal Consistency, Concurrency Control, Overview of Commercial Real Time databases.

References:

1. Real Time Systems by Jane W. S. Liu, Pearson Education Publication.
2. Mall Rajib, "Real Time Systems", Pearson Education
3. Albert M. K. Cheng , "Real-Time Systems: Scheduling, Analysis, and Verification", Wiley.

ECS-087 Mobile Computing

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Unit I

Introduction, issues in mobile computing, overview of wireless telephony: cellular concept, GSM: air-interface, channel structure, location management: HLR-VLR, hierarchical, handoffs, channel allocation in cellular systems, CDMA, GPRS.

Unit II

Wireless Networking, Wireless LAN Overview: MAC issues, IEEE 802.11, Blue Tooth, Wireless multiple access protocols, TCP over wireless, Wireless applications, data broadcasting, Mobile IP, WAP: Architecture, protocol stack, application environment, applications.

Unit III

Data management issues, data replication for mobile computers, adaptive clustering for mobile wireless networks, File system, Disconnected operations.

Unit IV

Mobile Agents computing, security and fault tolerance, transaction processing in mobile computing environment.

Unit V

Adhoc networks, localization, MAC issues, Routing protocols, global state routing (GSR), Destination sequenced distance vector routing (DSDV), Dynamic source routing (DSR), Ad Hoc on demand distance vector routing (AODV), Temporary ordered routing algorithm (TORA), QoS in Ad Hoc Networks, applications.

References:

1. J. Schiller, Mobile Communications, Addison Wesley.
2. Charles Perkins, Mobile IP, Addison Wesley.
3. Charles Perkins, Ad hoc Networks, Addison Wesley.
4. Upadhyaya, "Mobile Computing", Springer

ECS-851 Artificial Intelligence Lab

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1. AIM: TurboProlog features and format.
2. AIM: WAPusing variables in Prolog.
3. AIM: WAP for Usage of rules in Prolog.
4. AIM: WAP for using Inputs, Output and fail predicates in prolog, display:
 - (i) List of married and unmarried employees
 - (ii) List of male and female employees
 - (iii)List of employees for given job location
5. AIM: Create a small set of facts and rules on who is the ancestor of whom, Display:
 - (i) Who is ancestor of given person.
 - (ii) Complete list i.e. who is ancestor of whom